Alternator Voltage Regulator FET Driver

The CS3361 integral alternator regulator integrated circuit provides the voltage regulation for automotive, 3–phase alternators.

It drives an external logic level N channel enhancement power FET for control of the alternator field current. In the event of a charge fault, a lamp output pin is provided to drive an external darlington transistor capable of switching on a fault indicator lamp. An overvoltage or no Stator signal condition activates the lamp output.

This IC has customized current sense circuitry enabling it to drive FET transistors. The CS3361 is available in an SOIC–14 package.

Features

- Drives Logic Level Power NFET
- 80 V Load Dump
- Temperature Compensated Regulation Voltage
- Shorted Field Protection Duty Cycle, Self Clearing
- This is a Pb–Free Device



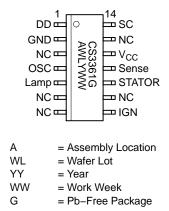
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SOIC-14 D SUFFIX CASE 751A





ORDERING INFORMATION

Device	Package	Shipping [†]
CS3361YDR14G	SOIC-14 (Pb-Free)	2500 / Tape & Reel

+For information on tape and reel specifications, including part orientation and tape sizes, please refer to our Tape and Reel Packaging Specification Brochure, BRD8011/D.

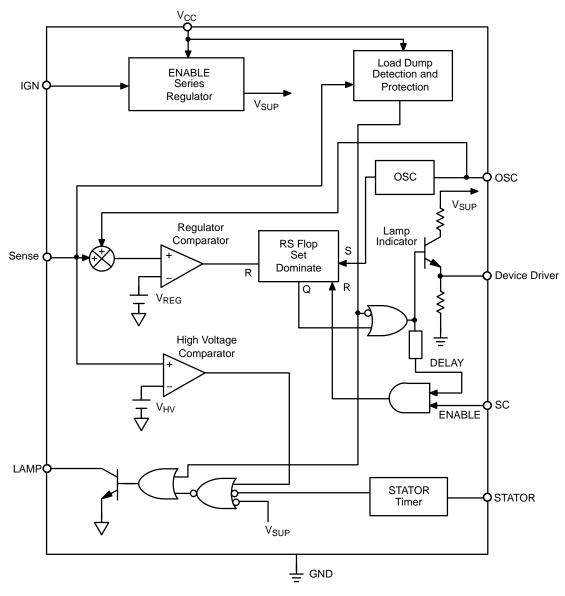


Figure 1. Block Diagram

PACKAGE PIN DESCRIPTION

PIN NO.	PIN SYMBOL	FUNCTION
1	Driver	Output driver for external power switch.
2	GND	Ground.
3, 6, 7, 9, 13	NC	No Connection.
4	OSC	Timing capacitor for oscillator.
5	Lamp	Base driver for lamp driver indicates no stator signal or overvoltage condition.
8	IGN	Switched ignition power up.
10	Stator	Stator signal input for stator timer.
11	Sense	Battery sense voltage regulator comparator input and protection.
12	V _{CC}	Supply for IC.
14	SC	Short circuit sensing.

MAXIMUM RATINGS

Rating	Value	Unit
Storage Temperature Range, T _S	-55 to +165	°C
Junction Temperature Range	-40 to 150	°C
Continuous Supply	27	V
I _{CC} Load Dump (@ V _{CC} = 80 V _{peak})	400	mA
Lead Temperature Soldering: Reflow: (SMD styles only) (Note 1)	260 peak	°C

Stresses exceeding those listed in the Maximum Ratings table may damage the device. If any of these limits are exceeded, device functionality should not be assumed, damage may occur and reliability may be affected. 1. 60 second maximum above 183°C.

PACKAGE THERMAL DATA

Parameter		SO-14	Unit
R _{OJC}	Typical	30	°C/W
R _{OJA}	Typical	125	°C/W

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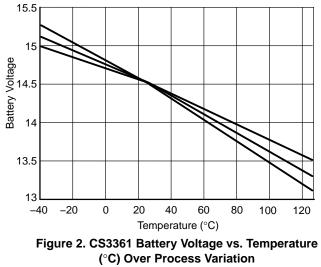
ELECTRICAL CHARACTERISTICS	$(-40^{\circ}C < T_{\Lambda} <$	125°C. –40°C < T	$< 150^{\circ}$ C. 9.0 V \leq V _{CC} \leq 17 V	unless otherwise specified.)

Characteristic	Characteristic Test Conditions		Тур	Мах	Unit	
Supply		·				
Supply Current Enabled	-	-	_	10	mA	
Supply Current Disabled	-	-	_	50	μA	
Driver Stage	·	ŀ				
Output High Voltage		5.5	_	12	V	
Output Low Voltage	I _{OL} = 25 μA	-	_	0.35	V	
Output High Current	V _{DD} = 1.2 V	-10	-6.0	-4.0	mA	
Minimum ON Time	C _{OSC} = 0.022 μF	200	-	-	μs	
Minimum Duty Cycle	-	_	6.0	10	%	
Short Circuit Duty Cycle	_	1.0	-	5.0	%	
Field Switch Turn On Rise Time	_	15	_	75	μs	
Field Switch Turn On Fall Time	_	15	_	75	μs	
Stator	·	·				
Input High Voltage		10	_	_	V	
Input Low Voltage	-	-	_	6.0	V	
Stator Time Out	High to Low	6.0	100	600	ms	
Lamp						
Output High Current	V _{LAMP} @ 3.0 V	-	-	50	μΑ	
Output Low Voltage	I _{LAMP} @ 30 mA	-	-	0.35	V	
Ignition		·				
Input High Voltage	I _{CC} > 1.0 mA	1.8	-	-	V	
Input Low Voltage	I _{CC} < 100 μA	-	-	0.5	V	
Oscillator						
Oscillator Frequency	C _{OSC} = 0.022 μF	90	-	210	Hz	
Rise Time/Fall Time	C _{OSC} = 0.022 μF	-	17	_	-	
Oscillator High Threshold	C _{OSC} = 0.022 μF	-	_	4.5	V	
Battery Sense						
Input Current	-	-10	-	+10	μΑ	
Regulation Voltage	@25°C, R ₁ = 100 kΩ, R ₂ = 50 kΩ	13.8	-	15.8	V	
Proportional Control	-	0.10	-	0.25	V	
High Voltage Threshold Ratio	VHigh Voltage@LampOn VRegulation@50%Duty Cycle	1.083	_	1.190	V/V	
High Voltage Hysteresis		0.020	_	0.600	V	

Product parametric performance is indicated in the Electrical Characteristics for the listed test conditions, unless otherwise noted. Product performance may not be indicated by the Electrical Characteristics if operated under different conditions.

CS3361

TYPICAL PERFORMANCE CHARACTERISTICS



APPLICATIONS INFORMATION

The CS3361 is designed for use in an alternator charging system.

In a standard alternator design (Figure 3), the rotor carries the field winding. An alternator rotor usually has several N and S poles. The magnetic field for the rotor is produced by forcing current through a field or rotor winding. The Stator windings are formed into a number of coils spaced around a cylindrical core. The number of coils equals the number of pairs of N and S poles on the rotor. The alternating current in the Stator windings is rectified by the diodes and applied to the regulator. By controlling the amount of field current, the magnetic field strength is controlled and hence the output voltage of the alternator.

Referring to Figure 7, a typical application diagram, the oscillator frequency is set by an external capacitor connected between OSC and ground. The sawtooth waveform ramps between 1.0 V and 3.0 V and provides the timing for the system. For the circuit shown the oscillator frequency is approximately 140 Hz. The alternator voltage is sensed at Terminal A via the resistor divider network R1/R2 on the Sense pin of the IC. The voltage at the sense pin determines the duty cycle for the regulator. The voltage on the sense pin causes a long duty cycle that increases the Field current. A high voltage results in a short duty cycle.

The ignition Terminal (I) switches power to the IC through the V_{CC} pin. The Stator pin monitors the voltage from the stator and senses a stopped engine condition. It drives the Lamp pin high after the stator timeout expires. The Lamp pin also goes high when an overvoltage condition

is detected on the sense pin. This causes the darlington lamp drive transistor to switch on and pull current through the lamp. If the system voltage continues to increase, the field and lamp output turn off as in an overvoltage or load dump condition.

The SC or Short Circuit pin monitors the field voltage. If the drive output and the SC voltage are simultaneously high for a predetermined period, a short circuit condition is assumed and the output is disabled. The regulator is forced to a minimum short circuit duty cycle.

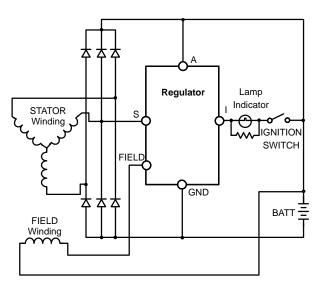


Figure 3. IAR System Block Diagram

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REGULATION WAVEFORMS

The CS3361 utilizes proportion control to maintain regulation. Waveforms depicting operation are shown in Figures 4, 5 and 6, where $V_{BAT/N}$ is the divided down voltage present on the Sense pin using R1 and R2 (Figure 7). A sawtooth waveform is generated internally. The amplitude of this waveform is listed in the electric parameter section as proportion control. The oscillator voltage is summed with $V_{BAT/N}$, and compared with the internal voltage regulator (V_{REG}) in the regulation comparator which controls the field through the output "Device Driver."

Figure 4 shows typical steady-state operation. A 50% duty cycle is maintained.

Figure 5 shows the effect of a drop in voltage on ($V_{BAT/N}$ + V_{OSC}). Notice the duty cycle increase to the field drive.

Figure 6 shows the effect of an increase in voltage (above the regulation voltage) on ($V_{BAT/N} + V_{OSC}$). Notice the decrease in field drive.

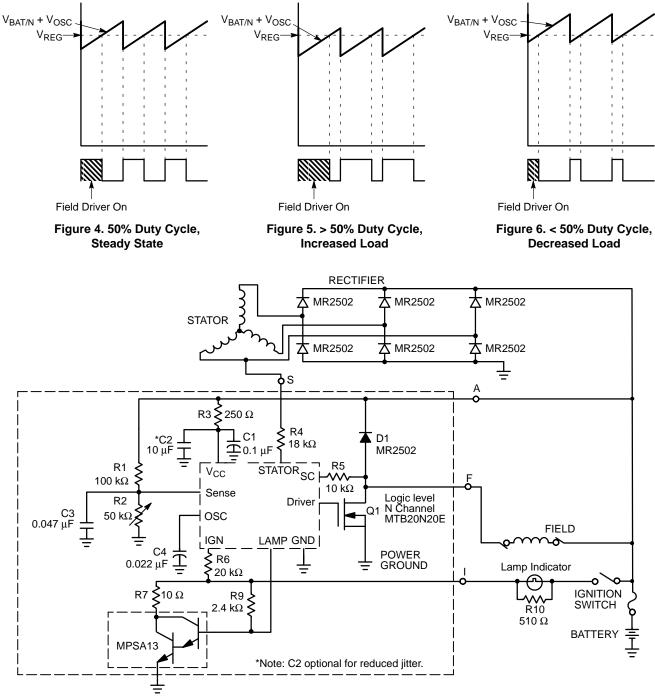


Figure 7. Typical Application Dlagram

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*For additional information on our Pb–Free strategy and soldering details, please download the **onsemi** Soldering and Mounting Techniques Reference Manual, SOLDERRM/D.

STYLES ON PAGE 2

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SOIC-14 CASE 751A-03 ISSUE L

DATE 03 FEB 2016

STYLE 1: PIN 1. COMMON CATHODE 2. ANODE/CATHODE 3. ANODE/CATHODE 4. NO CONNECTION 5. ANODE/CATHODE 6. NO CONNECTION 7. ANODE/CATHODE 8. ANODE/CATHODE 9. ANODE/CATHODE 10. NO CONNECTION 11. ANODE/CATHODE 12. ANODE/CATHODE 13. NO CONNECTION 14. COMMON ANODE	STYLE 2: CANCELLED	STYLE 3: PIN 1. NO CONNECTION 2. ANODE 3. ANODE 4. NO CONNECTION 5. ANODE 6. NO CONNECTION 7. ANODE 8. ANODE 9. ANODE 10. NO CONNECTION 11. ANODE 12. ANODE 13. NO CONNECTION 14. COMMON CATHODE	STYLE 4: PIN 1. NO CONNECTION 2. CATHODE 3. CATHODE 4. NO CONNECTION 5. CATHODE 6. NO CONNECTION 7. CATHODE 8. CATHODE 10. NO CONNECTION 11. CATHODE 12. CATHODE 13. NO CONNECTION 14. COMMON ANODE
STYLE 5: PIN 1. COMMON CATHODE 2. ANODE/CATHODE 3. ANODE/CATHODE 4. ANODE/CATHODE 5. ANODE/CATHODE 6. NO CONNECTION 7. COMMON ANODE 8. COMMON CATHODE 9. ANODE/CATHODE 10. ANODE/CATHODE 11. ANODE/CATHODE 12. ANODE/CATHODE 13. NO CONNECTION 14. COMMON ANODE	STYLE 6: PIN 1. CATHODE 2. CATHODE 3. CATHODE 4. CATHODE 5. CATHODE 6. CATHODE 7. CATHODE 9. ANODE 10. ANODE 11. ANODE 12. ANODE 13. ANODE 14. ANODE	STYLE 7: PIN 1. ANODE/CATHODE 2. COMMON ANODE 3. COMMON CATHODE 4. ANODE/CATHODE 5. ANODE/CATHODE 6. ANODE/CATHODE 7. ANODE/CATHODE 9. ANODE/CATHODE 10. ANODE/CATHODE 11. COMMON CATHODE 12. COMMON CATHODE 13. ANODE/CATHODE 14. ANODE/CATHODE	STYLE 8: PIN 1. COMMON CATHODE 2. ANODE/CATHODE 3. ANODE/CATHODE 4. NO CONNECTION 5. ANODE/CATHODE 6. ANODE/CATHODE 7. COMMON ANODE 9. ANODE/CATHODE 10. ANODE/CATHODE 11. NO CONNECTION 12. ANODE/CATHODE 13. ANODE/CATHODE 14. COMMON CATHODE

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