





# JN Semiconductor®

To kara more about Old Semiconductor, please visit our website at

Please note. As part of the Fairchild Semiconductor integration, some of the Fairchild orderable part numbers will need to change in order to meet ON Semiconductor's system requirements. Since the ON Semiconductor product management systems do not have the ability to manage part nomenclature that utilizes an underscore (\_), the underscore (\_) in the Fairchild part numbers will be changed to a dash (-). This document may contain device numbers with an underscore (\_). Please check the ON Semiconductor website to verify the updated device numbers. The most current and up-to-date ordering information can be found at <a href="www.onsemi.com">www.onsemi.com</a>. Please email any questions regarding the system integration to Fairchild <a href="guestions@onsemi.com">questions@onsemi.com</a>.

ON Semiconductor and the ON Semiconductor logo are trademarks of Semiconductor Components Industries, LLC dba ON Semiconductor or its subsidiaries in the United States and/or other countries. ON Semiconductor owns the rights to a number of patents, trademarks, copyrights, trade secrets, and other intellectual property. A listing of ON Semiconductor's product/patent coverage may be accessed at www.onsemi.com/site/pdf/Patent-Marking.pdf. ON Semiconductor reserves the right to make changes without further notice to any products herein. ON Semiconductor makes no warranty, representation or guarantee regarding the suitability of its products for any particular purpose, nor does ON Semiconductor assume any liability arising out of the application or use of any product or circuit, and specifically disclaims any and all liability, including without limitation special, consequential or incidental damages. Buyer is responsible for its products and applications using ON Semiconductor products, including compliance with all laws, regulations and safety requirements or standards, regardless of any support or applications information provided by ON Semiconductor. "Typical" parameters which may be provided in ON Semiconductor data sheets and/or specifications can and do vary in different applications and actual performance may vary over time. All operating parameters, including "Typicals" must be validated for each customer application by customer's technical experts. ON Semiconductor does not convey any license under its patent rights nor the rights of others. ON Semiconductor products are not designed, intended, or authorized for use as a critical component in life support systems or any EDA Class 3 medical devices or medical devices with a same or similar classification in a foreign jurisdiction or any devices intended for implantation in the human body. Should Buyer purchase or use ON Semiconductor products for any such unintended or unauthorized application, Buyer shall indemnify and hold ON Semiconductor and its officer



May 2024

# FAN7371 High-Current High-Side Gate Drive IC

#### **Features**

- Floating Channel for Bootstrap Operation to +600V
- 4A/4A Sourcing/Sinking Current Driving Capability
- Common-Mode dv/dt Noise Canceling Circuit
- 3.3V and 5V Input Logic Compatible
- Output In-phase with Input Signal
- Under- Voltage Lockout for V<sub>BS</sub>
- 25V Shunt Regulator on V<sub>DD</sub> and V<sub>BS</sub>
- 8-Lead Small Outline Package (SOP)

### **Applications**

- High-Speed Gate Driver
- Sustaine Switch Driver in PDP oplication
- High-Power F J. Con rter
- Motor Drive was

### Description

The FAN7371 is a monolithic an-side rate drive IC, which can drive high-sper MO TETS and IGBTs (nat operate up to +600V. If as a fuffer the put stage with all NMOS transisting capability and minimum cass-conduction.

Fairchild' his voltas process and common-mode noise cant 'ing the jues provide stable operation of 'he in driver under high dv't noise circumter and An advanced level-shift circuit offers high-side gath or operation up to  $V_3$ =-9.8V (typical) for  $V_{BS}$ = 5V.

The UVLO circuit prevents malfunction when  $V_{BS}$  is lower than the specified threshold voltage.

The high current and low-output voltage drop feature makes this device satisfies for sustaine switch driver and energy recovery switch driver in the Plasma Display Panel application, motor drive inverter, switching power supply, and high-power DC-DC converter applications.

8-SOP



# **Crdering Information**

Ŋ.						
	Part Number	Package	Operating Temperature Range	© Eco Status	Packing Method	
	FAN7371M <sup>(1)</sup>	8-SOP	-40°C ~ 125°C	RoHS	Tube	
	FAN7371MX <sup>(1)</sup>	0-30F	-40 C ~ 125 C	Kulis	Tape & Reel	

#### Note:

1. These devices passed wave soldering test by JESD22A-111.



For Fairchild's definition of Eco Status, please visit: http://www.fairchildsemi.com/company/green/rohs\_green.html.

# **Typical Application Diagrams**

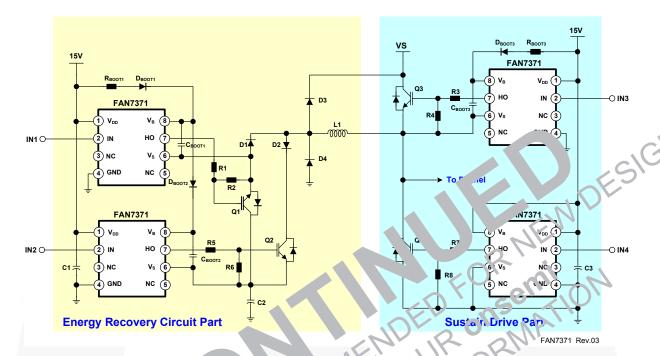


Figure 1. Floate Bidire ion 'Switch and Half-Bridge Driver: PDP application

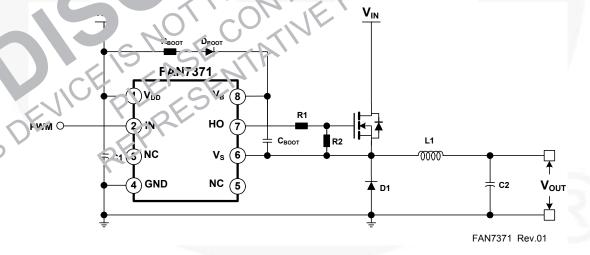
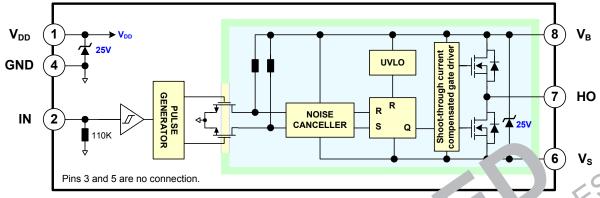


Figure 2. Step-Down (Buck) DC-DC Converter Application

# **Internal Block Diagram**



. AN731 .xev.04

Figure 3. Functional Block Diagr.

# **Pin Configuration**

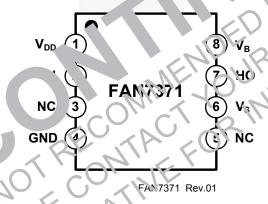


Figure 4. Pin Configuration (Top View)

# Pin Definitions

Pin#	Name	Description
1	Vion	Supply Voltage
2	IN	Logic Input for High-Side Gate Driver Output
3	NC	No Connection
4	GND	Ground
5	NC	No Connection
6	V <sub>S</sub>	High-Voltage Floating Supply Return
7	НО	High-Side Driver Output
8	V <sub>B</sub>	High-Side Floating Supply

### **Absolute Maximum Ratings**

Stresses exceeding the absolute maximum ratings may damage the device. The device may not function or be operable above the recommended operating conditions and stressing the parts to these levels is not recommended. In addition, extended exposure to stresses above the recommended operating conditions may affect device reliability. The absolute maximum ratings are stress ratings only.  $T_A=25^{\circ}C$  unless otherwise specified.

Symbol	Characteristics	Min.	Max.	Unit
V <sub>S</sub>	High-Side Floating Offset Voltage	V <sub>B</sub> -V <sub>SHUNT</sub>	V <sub>B</sub> +0.3	V
V <sub>B</sub>	High-Side Floating Supply Voltage <sup>(2)</sup>	-0.3	625.0	V
V <sub>HO</sub>	High-Side Floating Output Voltage	V <sub>S</sub> -0.3	V <sub>B</sub> +0.3	V
$V_{DD}$	Low-Side and Logic Supply Voltage <sup>(2)</sup>	-0.3	V <sub>SHUNT</sub>	V
V <sub>IN</sub>	Logic Input Voltage	-0.3	V <sub>D</sub> r	V
dV <sub>S</sub> /dt	Allowable Offset Voltage Slew Rate		50	V/ns
P <sub>D</sub>	Power Dissipation <sup>(3, 4, 5)</sup>		J.0.	W
$\theta_{JA}$	Thermal Resistance		200	°C \VV
$T_J$	Junction Temperature	-5.	+150	oC .
T <sub>STG</sub>	Storage Temperature	-55	-150	°C
T <sub>A</sub>	Operating Ambient Temperature		+125	°C

#### Notes:

- 2 This IC contains a shunt regulator on V<sub>DD</sub> and <sub>BS</sub> ith a number alknown voltage contains a shunt regulator on V<sub>DD</sub> and <sub>BS</sub> ith a number alknown voltage contains a shunt regulator on V<sub>DD</sub> and <sub>BS</sub> ith a number alknown voltage contains a shunt regulator on V<sub>DD</sub> and <sub>BS</sub> ith a number alknown voltage contains a shunt regulator on V<sub>DD</sub> and <sub>BS</sub> ith a number alknown voltage contains a shunt regulator on V<sub>DD</sub> and <sub>BS</sub> ith a number alknown voltage contains a shunt regulator on V<sub>DD</sub> and <sub>BS</sub> ith a number alknown voltage contains a shunt regulator on V<sub>DD</sub> and <sub>BS</sub> ith a number alknown voltage contains a shunt regulator on V<sub>DD</sub> and <sub>BS</sub> ith a number alknown voltage contains a shunt regulator on V<sub>DD</sub> and <sub>BS</sub> ith a number alknown voltage contains a shunt regulator on V<sub>DD</sub> and <sub>BS</sub> ith a number alknown voltage contains a shunt regulator on V<sub>DD</sub> and <sub>BS</sub> ith a number alknown voltage contains a shunt regulator of the shunt regu
- 3 Mounted on 76.2 x 114.3 x 1.6mm PCL Fr. ss epoxy material).
- 4 Refer to the following stand: us:

  JESD51-2: Integral circuits = armal to method environmental conditions, natural convection, and JESD51-3: Low effortunate the large uctivity test board for readed surface mount packages.
- 5 Do not exceed po er dissination (רֹם) under any circumstances

# Recomi. anded / perating Conditions

Recommended Operating Conditions table defines the conditions for actual device operation. Recommended on rating undersonable processing them codes igning to absolute maximum ratings.

Symbol	Parameter	Min.	Max.	Unit
V'3.	High-Side Floeting Supply Voltage	V <sub>S</sub> +10	V <sub>S</sub> +20	V
V <sub>S</sub>	High-Side Floating Supply Offset Voltage	6-V <sub>DD</sub>	600	V
V <sub>HO</sub>	High Side Output Voltage	V <sub>S</sub>	$V_{B}$	V
V <sub>IN</sub>	Logic Input Voltage	GND	$V_{DD}$	V
V <sub>DD</sub>	Supply Voltage	10	20	V

### **Electrical Characteristics**

 $V_{BIAS}(V_{DD}, V_{BS})$ =15.0V,  $T_A$  = 25°C, unless otherwise specified. The  $V_{IN}$  and  $I_{IN}$  parameters are referenced to GND. The  $V_O$  and  $I_O$  parameters are relative to  $V_S$  and are applicable to the respective output HO.

Symbol	Characteristics	Test Condition	Min.	Тур.	Max.	Unit
POWER	SUPPLY SECTION			L	l	
$I_{QDD}$	Quiescent V <sub>DD</sub> Supply Current	V <sub>IN</sub> =0V or 5V		25	70	μΑ
I <sub>PDD</sub>	Operating V <sub>DD</sub> Supply Current	f <sub>IN</sub> =20KHz, No Load		35	100	μΑ
300TS1	RAPPED SUPPLY SECTION			I		
V <sub>BSUV+</sub>	V <sub>BS</sub> Supply Under-Voltage Positive Going Threshold Voltage	V <sub>BS</sub> =Sweep	8.2	9.2	10.2	V
V <sub>BSUV</sub> -	V <sub>BS</sub> Supply Under-Voltage Negative Going Threshold Voltage	V <sub>BS</sub> =Sweep	7.5	-	9.5	٧
V <sub>BSHYS</sub>	V <sub>BS</sub> Supply Under-Voltage Lockout Hysteresis Voltage	V <sub>BS</sub> =Sweep		0.7		Ċ
I <sub>LK</sub>	Offset Supply Leakage Current	V <sub>B</sub> =V <sub>S</sub> =600V			10	μΑ
I <sub>QBS</sub>	Quiescent V <sub>BS</sub> Supply Current	V <sub>IN</sub> =0V or 5V		රිට	120	μΑ
I <sub>PBS</sub>	Operating V <sub>BS</sub> Supply Current	C <sub>LOAD</sub> =1nF, f <sub>II</sub> 20t z, rm: Value	26	1.0	2.8	mA
HUNT R	EGULATOR SECTION			10		
V <sub>SHUNT</sub>	V <sub>DD</sub> and V <sub>BS</sub> Shunt Regulator Clamping Voltage	'HUNT'- "A	20	25	OL	V
NPUT LO	OGIC SECTION	0 - 20.		12/		
V <sub>IH</sub>	Logic "1" Input Voltage	SM 18	3.5			V
V <sub>IL</sub>	Logic "0" Input Voltage	100,00	3/		0.8	V
I <sub>IN+</sub>	Logic Input High Bias ( rrent	V <sub>III</sub> =5V		45	70	μΑ
I <sub>IN-</sub>	Logic Input Lr Blus CL Int	V <sub>IN</sub> =0∨			2	μΑ
R <sub>IN</sub>	Input Pull-do Resis nce	100	70	110		ΚΩ
ATE DR	RIVE OUTPUT TOT ON			I	I	
V <sub>OH</sub>	Tign. Jore tput Voltage (ValAS - Vo)	No Locid			1.2	V
	L. LC utput Volt-ge	No Load			30	mV
Ī <sub>O+</sub>	Dut, & High, Short-Circuit Pulsed Current (3)	V <sub>HO</sub> =0V, V <sub>IN</sub> =5V, PW ≤10μs	3.0	4.0		Α
	Output Low. Short-Circuit Pulsed Current(6)	V <sub>HO</sub> =15V,V <sub>IN</sub> =0V, PW ≤10μs	3.0	4.0		Α
Vs Allowable Negative Vs pin Voltage for IN Signal Propagation to HC				-9.8	-7.0	V
lote:		•	•	•		

Note

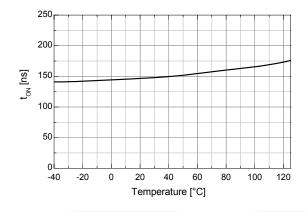
These parameters graranteed by design.

### **Dynamic Electrical Characteristics**

 $V_{DD}$ = $V_{BS}$ =15V, GND=0V,  $C_{LOAD}$ =1000pF,  $T_{A}$ =25°C, unless otherwise specified.

Symbol	Parameter	Conditions	Min.	Тур.	Max.	Unit
t <sub>on</sub>	Turn-on Propagation Delay Time	V <sub>S</sub> =0V		150	210	ns
t <sub>off</sub>	Turn-off Propagation Delay Time	V <sub>S</sub> =0V		150	210	ns
t <sub>r</sub>	Turn-on Rise Time			25	50	ns
t <sub>f</sub>	Turn-off Fall Time			15	40	ns

# **Typical Characteristics**



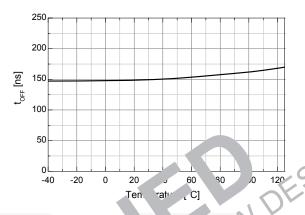
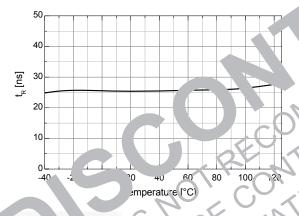


Figure 5. Turn-on Propagation Delay vs. Temperature

Figu 6. Tu -ot. pagetion Delay vs. emperature



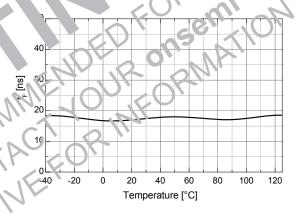
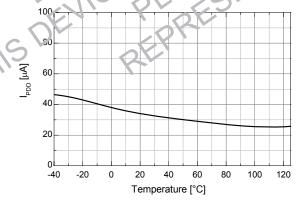


Figure 8. Turn-off Fall Time vs. Temperature



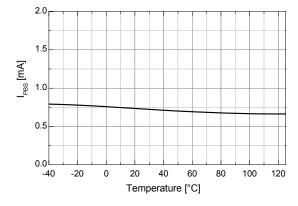
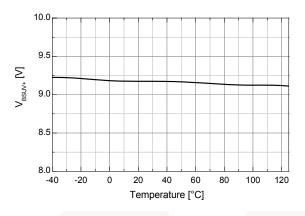


Figure 9. Operating  $V_{DD}$  Supply Current vs. Temperature

Figure 10. Operating V<sub>BS</sub> Supply Current vs. Temperature

# **Typical Characteristics** (Continued)



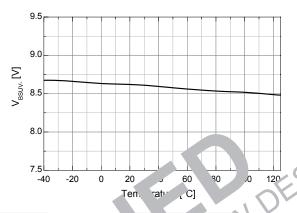
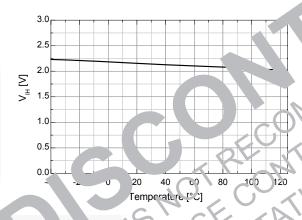
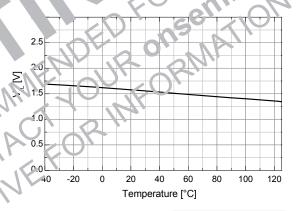


Figure 11. V<sub>BS</sub> UVLO+ vs. Temperature

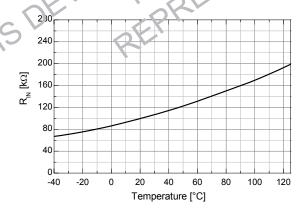
vs. Temperature Figu. 12. V<sub>L</sub> UV.





ure 13 Logic High lippur Voltage vs. Temperature

Figure 14. Logic Low Input Voltage vs. Temperature



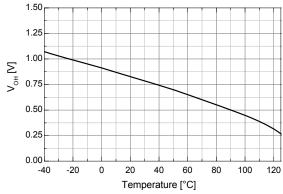
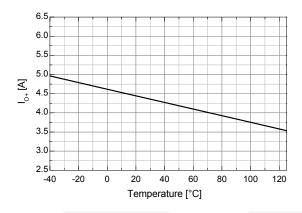


Figure 15. Input Pull-Down Resistance vs.Temperature.

Figure 16. High-Level Output Voltage vs. Temperature

# **Typical Characteristics** (Continued)



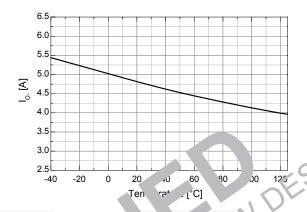
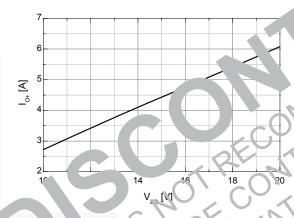
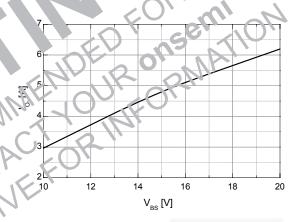


Figure 17. Output High, Short-Circuit Pulsed Current vs. Temperature

Figure 18. C 'out L. ', S. Circuit Pulsed Current vs. emperature





i Ture 15 Dui\_ ut High, Snort-Circuit Pursed Gurrent vs. Supply Voltage

Figure 20. Output Low, Short-Circuit Pulsed Current vs. Supply Voltage

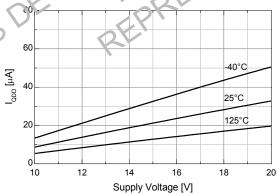


Figure 21. Quiescent V<sub>DD</sub> Supply Current vs. Supply Voltage

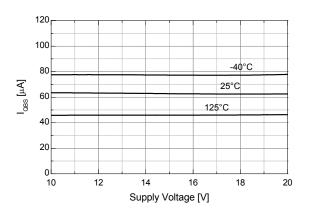


Figure 22. Quiescent V<sub>BS</sub> Supply Current vs. Supply Voltage

# **Switching Time Definitions**

### **Timing Diagram**

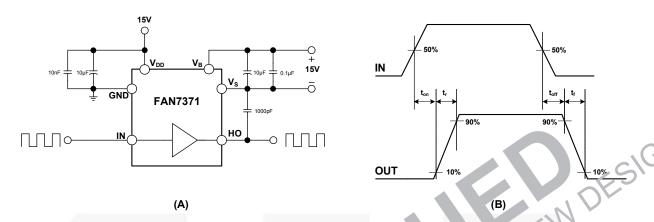


Figure 23. Switching Time Test Circuit and 'av rm D initions

# **Physical Dimensions**

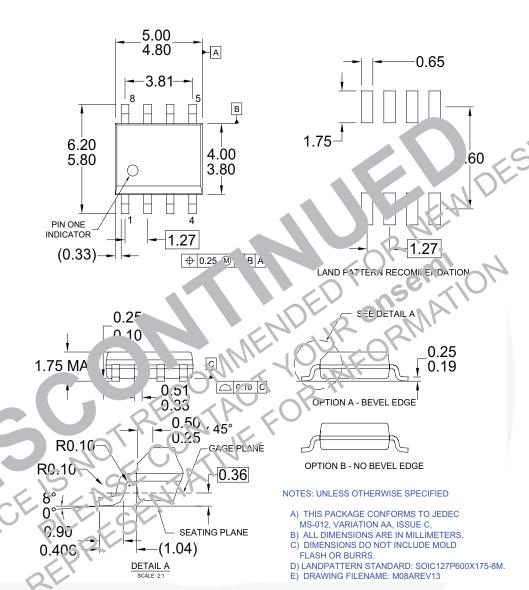


Figure 24. 8-Lead Small Outline Package (SOP)

Package drawings are provided as a service to customers considering Fairchild components. Drawings may change in any manner without notice. Please note the revision and/or date on the drawing and contact a Fairchild Semiconductor representative to verify or obtain the most recent revision. Package specifications do not expand the terms of Fairchild's worldwide terms and conditions, specifically the warranty therein, which covers Fairchild products.

Always visit Fairchild Semiconductor's online packaging area for the most recent package drawings: http://www.fairchildsemi.com/packaging/.





#### TRADEMARKS

The following includes registered and unregistered trademarks and service marks, owned by Fairchild Semiconductor and/or its global subsidiaries, and is not intended to be an exhaustive list of all such trademarks.

Intended to be an exhaustive list of AccuPower™
Auto-SPM™
Build it Now™
CorePLUS™
CorePOWER™
CROSSVOLT™
CTL™

CTLTM
Current Transfer LogicTM
EcoSPARK®
EfficientMaxTM
EZSWITCHTM\*

EZITM\*
DELIXPERDIM

Fairchild®
Fairchild Semiconductor®
FACT Quiet Series™
FACT®

FAST<sup>®</sup> FastvCore™ FETBench™ FlashWriter®\*
FPS™
F-PFS™
FRFET®

Global Power Resource<sup>SM</sup> Green FPS™ Green FPS™ e-Series™ G*m*ax™

GMAXIM
GTOTM
IntelliMAXTM
ISOPLANARTM
MegaBuckTM
MICROCOUPLERTM
MicroFETTM
MicroPakTM
MillerDriveTM

MillerDrive™ MotionMax™ Motion-SPM™ OPTOLOGIC® OPTOPLANAR®

PDP SPM™

Power-SPM™ PowerTrench® PowerXS™

Programmable Active Droop™

QFET® QS™ Quiet Series™ RapidConfigure™

Saving our world, 1mW/W/kW at a time™ SignalWise™ SmartMax™ SMART START™ SPM® STEALTH™

SPM
STEALTH™
SuperFET™
SuperSOT™.3
SuperSOT™.6
SuperSOT™.8
SupreMC.
SVM. TTM

The Power Franchise®

The Wer

franchise

TinyBoost™

TinyBuck™

TinyCalc™

Timd ogic®

TimyPum

TinyPum

TinyPum

TinyPum

TinyPum

ThuECURRENT

LSerDes™

SYSTEM ®\*\*

Ultra FRFET™ UniFFT™ ''':'\x'™ ''isualMax'™

#### DISCLAIMER

#### LIFE SUPPORT POLICY

FAIRCHILD'S PROD' REINTAUTH RIZED FOR USE AS CRITICAL COMPONENTS NULFE SUPPORT DEVICES OR SYSTEMS WITHOUT THE EXPRESS WRITTE APPROVAL HILD SEMIC INDUCTOR CORPORATION.

#### As used hereil

- 1. 'I rupp, devir her istems the devices or systems which, (a) are intermited. If or rigidal implant into the body or (ii) support or sustain life, and (c) hos failure to perform when plopper used in accordance with insections for use provided in the labeling, can be reasonably peet to result in a significant injury in the user.
- A critical component in any component of a life support, device, or system whose failure to perform can be reasonably expected to cause the failure of the life support device or system, or to affect its safety or effectiveness.

#### ANTI-C JUNTERI'ETING POLICY

Fairchild Servicond ctor Corporation's Anti-Counterfeiting Policy. Fairchild's Anti-Counterfeiting Policy is also stated on our external website, www.fairchildsemi.com, under Saics Support.

Counterleiting of semiconductor parts is a growing problem in the industry. All manufacturers of semiconductor products are experiencing counterfeiting of their parts. Customers who inadvertently purchable counterfeit parts experience many problems such as loss of brand reputation, substandard performance, failed applications, and increased cost of production and manufacturing delays. Fairchild is taking strong measures to protect ourselves and our customers from the proliferation of counterfeit parts. Fairchild strongly encourages customers to purchase Fairchild parts either directly from Fairchild or from Authorized Fairchild Distributors who are listed by country on our web page cited above. Products customers buy either from Fairchild directly or from Authorized Fairchild Distributors are genuine parts, have full traceability, meet Fairchild's quality standards for handling and storage and provide access to Fairchild's full range of up-to-date technical and product information. Fairchild and our Authorized Distributors will stand behind all warranties and will appropriately address any warranty issues that may arise. Fairchild will not provide any warranty coverage or other assistance for parts bought from Unauthorized Sources. Fairchild is committed to combat this global problem and encourage our customers to do their part in stopping this practice by buying direct or from authorized distributors.

#### PRODUCT STATUS DEFINITIONS

#### **Definition of Terms**

Datasheet Identification	Product Status	Definition
Advance Information	Formative / In Design	Datasheet contains the design specifications for product development. Specifications may change in any manner without notice.
Preliminary	First Production	Data sheet contains preliminary data; supplementary data will be published at a later date. Fairchild Semiconductor reserves the right to make changes at any time without notice to improve design.
No Identification Needed	Full Production	Datasheet contains final specifications. Fairchild Semiconductor reserves the right to make changes at any time without notice to improve the design.
		Datasheet contains specifications on a product that is discontinued by Fairchild Semiconductor. The datasheet is for reference information only.

Rev. 143 www.fairchildsemi.com

<sup>\*</sup> Trademarks of System General Corporation, used under licer by Fairc Semice Luctor.



ON Semiconductor and the are trademarks of Semiconductor Components Industries, LLC dba ON Semiconductor or its subsidiaries in the United States and/or other countries. ON Semiconductor owns the rights to a number of patents, trademarks, copyrights, trade secrets, and other intellectual property. A listing of ON Semiconductor's product/patent coverage may be accessed at <a href="https://www.onsemi.com/site/pdf/Patent-Marking.pdf">www.onsemi.com/site/pdf/Patent-Marking.pdf</a>. ON Semiconductor reserves the right to make changes without further notice to any products herein. ON Semiconductor and severally, representation or guarantee regarding the suitability of its products for any particular purpose, nor does ON Semiconductor assume any liability arising out of the application or use of any product or circuit, and specifically disclaims any and all liability, including without limitation special, consequential or incidental damages. Buyer is responsible for its products and applications using ON Semiconductor products, including compliance with all laws, regulations and safety requirements or standards, regardless of any support or applications information provided by ON Semiconductor. "Typical" parameters which may be provided in ON Semiconductor data sheets and/or specifications can and do vary in different applications and actual performance may vary over time. All operating parameters, including "Typicals" must be validated for each customer application by customer's technical experts. ON Semiconductor does not convey any license under its patent rights nor the rights of others. ON Semiconductor products are not designed, intended, or authorized for use as a critical component in life support systems or any FDA Class 3 medical devices or medical devices with a same or similar classification in a foreign jurisdiction or any devices intended for implantation in the human body. Should Buyer purchase or use ON Semiconductor products for any such unintended or unauthorized application, Buyer shall indemnify an

#### **PUBLICATION ORDERING INFORMATION**

#### LITERATURE FULFILLMENT:

Literature Distribution Center for ON Semiconductor 19521 E. 32nd Pkwy, Aurora, Colorado 80011 USA Phone: 303-675-2175 or 800-344-3860 Toll Free USA/Canada Fax: 303-675-2176 or 800-344-3867 Toll Free USA/Canada Email: orderlit@onsemi.com

USA/Canada
Europe, Middle East and Africa Technical Support:
Phone: 421 33 790 2910
Japan Customer Focus Center
Phone: 81-3-5817-1050

N. American Technical Support: 800-282-9855 Toll Free

ON Semiconductor Website: www.onsemi.com

Order Literature: http://www.onsemi.com/orderlit

For additional information, please contact your local Sales Representative