

MOSFET - N-Channel, UniFET™

650 V, 15 A, 440 mΩ

FDPF15N65

Description

UniFET™ MOSFET is **onsemi**'s high voltage MOSFET family based on planar stripe and DMOS technology. This MOSFET is tailored to reduce on–state resistance, and to provide better switching performance and higher avalanche energy strength. This device family is suitable for switching power converter applications such as power factor correction (PFC), flat panel display (FPD) TV power, ATX and electronic lamp ballasts.

Features

- $R_{DS(on)} = 360 \text{ m}\Omega \text{ (Typ.)} @ V_{GS} = 10 \text{ V}, I_D = 7.5 \text{ A}$
- Low Gate Charge (Typ. 48.5 nC)
- Low C_{rss} (Typ. 23.6 pF)
- 100% Avalanche Tested

Applications

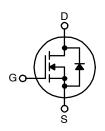
- LCD / LED / PDP TV and Monitor
- Uninterruptible Power Supply

V _{DSS}	R _{DS(on)} MAX	I _D MAX	
650 V	440 mΩ @ 10 V	15 A	

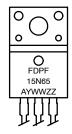


TO-220 Fullpack, 3-Lead / TO-220F-3SG CASE 221AT

N-CHANNEL MOSFET



MARKING DIAGRAM



FDPF15N65 = Specific Device Code
A = Assembly Location
YWW = Date Code (Year & Week)
ZZ = Assembly Lot

ORDERING INFORMATION

See detailed ordering and shipping information on page 8 of this data sheet.

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ABSOLUTE MAXIMUM RATINGS ($T_C = 25^{\circ}C$ unless otherwise noted)

Symbol	Parameter		Rating	Unit
V_{DSS}	Drain-Source Voltage		650	V
I _D	Drain Current $ \begin{array}{c} - \text{ Continuous } (T_C = 25^{\circ}\text{C}) \\ - \text{ Continuous } (T_C = 100^{\circ}\text{C}) \end{array} $		15* 9.5*	A A
I _{DM}	Drain Current - Pulsed (Note 1)		60*	Α
V_{GSS}	Gate-Source Voltage		±30	V
E _{AS}	Single Pulsed Avalanche Energy (Note 2)		637	mJ
I _{AR}	Avalanche Current (Note 1)		15	Α
E _{AR}	Repetitive Avalanche Energy (Note 1)		25.0	mJ
dv/dt	Peak Diode Recovery dv/dt (Note 3)		4.5	V/ns
P _D	Power Dissipation	(T _C = 25°C) – Derate Above 25°C	38.5 0.3	W W/°C
$T_{J_i}T_{STG}$	Operating and Storage Temperature Range		-55 to +150	°C
TL	Maximum Lead Temperature for Soldering, 1/8" from Case for 5 Seconds		300	°C

Stresses exceeding those listed in the Maximum Ratings table may damage the device. If any of these limits are exceeded, device functionality stresses exceeding those listed in the Maximum Hatings table may damage it should not be assumed, damage may occur and reliability may be affected. *Drain current limited by maximum junction temperature.
1. Repetitive rating: pulse–width limited by maximum junction temperature.
2. L = 5.23 mH, I_{AS} = 15 A, V_{DD} = 50 V, R_{G} = 25 Ω , starting T_{J} = 25°C.
3. $I_{SD} \le$ 15 A, di/dt \le 200 A/ μ s, $V_{DD} \le$ BV_{DSS}, starting T_{J} = 25°C.

THERMAL CHARACTERISTICS

Symbol	Parameter	Value	Unit
$R_{\theta JC}$	Thermal Resistance, Junction-to-Case, Max.	3.3	°C/W
$R_{\theta JA}$	Thermal Resistance, Junction-to-Ambient, Max.	62.5	°C/W

ELECTRICAL CHARACTERISTICS (T_C = 25°C unless otherwise noted)

Symbol	Parameter	Conditions	Min	Тур	Max	Unit
OFF CHAP	ACTERISTICS				-	
BV _{DSS}	Drain-Source Breakdown Voltage	$V_{GS} = 0 \text{ V}, I_D = 250 \mu\text{A}, T_J = 25^{\circ}\text{C}$	650	_	_	V
ΔBV _{DSS} / ΔT _J	Breakdown Voltage Temperature Coefficient	I_D = 250 μ A, Referenced to 25°C	-	0.65	_	V/°C
I _{DSS}	Zero Gate Voltage Drain Current	V _{DS} = 650 V, V _{GS} = 0 V V _{DS} = 520 V, T _C = 125°C		- -	1 10	μ Α μ Α
I _{GSSF}	Gate-Body Leakage Current, Forward	V _{GS} = 30 V, V _{DS} = 0 V	-	-	100	nA
I _{GSSR}	Gate-Body Leakage Current, Reverse	$V_{GS} = -30 \text{ V}, V_{DS} = 0 \text{V}$	-	-	-100	nA
N CHAR	ACTERISTICS				-	
V _{GS(th)}	Gate Threshold Voltage	$V_{DS} = V_{GS}, I_D = 250 \mu A$	3.0	-	5.0	V
R _{DS(on)}	Static Drain-Source On-Resistance	V _{GS} = 10 V, I _D = 7.5 A	-	0.36	0.44	Ω
9FS	Forward Transconductance	V _{DS} = 40 V, I _D = 7.5 A	1	19.2	-	S
YNAMIC	CHARACTERISTICS					-
C _{iss}	Input Capacitance	$V_{DS} = 25 \text{ V}, V_{GS} = 0 \text{ V}, f = 1 \text{ MHz}$	-	2380	3095	pF
C _{oss}	Output Capacitance	1	1	295	385	pF
C _{rss}	Reverse Transfer Capacitance	1	1	23.6	35.5	pF
WITCHIN	G CHARACTERISTICS					-
t _{d(on)}	Turn-On Delay Time	$V_{DD} = 325 \text{ V}, I_D = 15 \text{ A}, V_{GS} = 10 \text{ V},$	-	65	140	ns
t _r	Turn-On Rise Time	$R_G = 21.7 \Omega$ (Note 4)	-	125	260	ns
t _{d(off)}	Turn-Off Delay Time	1	-	105	220	ns
t _f	Turn-Off Fall Time]	_	65	140	ns
Qg	Total Gate Charge	V _{DS} = 520 V, I _D = 15 A, V _{GS} = 10 V	_	48.5	63.0	nC
Q _{gs}	Gate-Source Charge	(Note 4)	_	14.0	-	nC
Q _{gd}	Gate-Drain Charge]	_	21.2	-	nC
RAIN-SC	DURCE DIODE CHARACTERISTICS AND MAX	KIMUM RATINGS				
Is	Maximum Continuous Drain-Source Diode Forward Current		-	_	15*	Α
I _{SM}	Maximum Pulsed Drain-Source Diode Forward Current		-	-	60	Α
V_{SD}	Drain-Source Diode Forward Voltage	V _{GS} = 0 V, I _S = 15 A	-	-	1.4	V
t _{rr}	Reverse Recovery Time	$V_{GS} = 0 \text{ V, } I_S = 15 \text{ A,}$	-	496	-	ns
Q _{rr}	Reverse Recovery Charge	dl _F /dt = 100 A/μs	_	5.69	-	μС

Product parametric performance is indicated in the Electrical Characteristics for the listed test conditions, unless otherwise noted. Product performance may not be indicated by the Electrical Characteristics if operated under different conditions.

4. Essentially independent of operating temperature typical characteristics.

TYPICAL PERFORMANCE CHARACTERISTICS

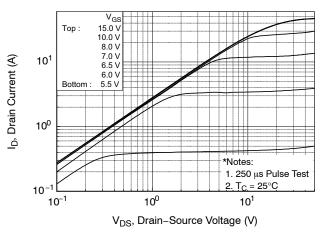


Figure 1. On-Region Characteristics

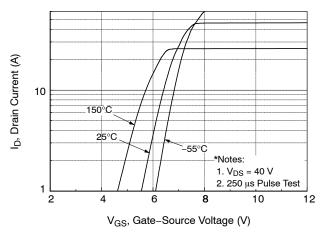


Figure 2. Transfer Characteristics

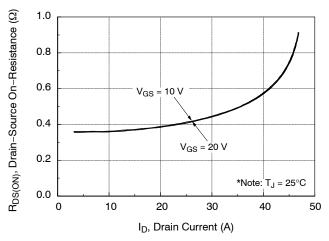


Figure 3. On-Resistance Variation vs. Drain Current and Gate Voltage

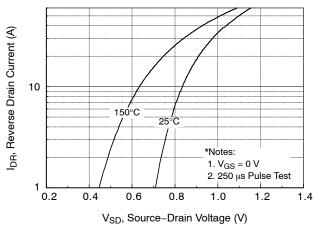


Figure 4. Body Diode Forward Voltage Variation vs. Source Current and Temperature

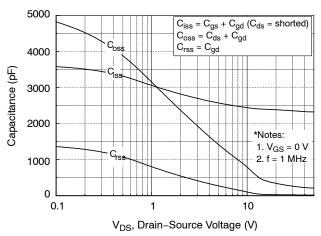


Figure 5. Capacitance Characteristics

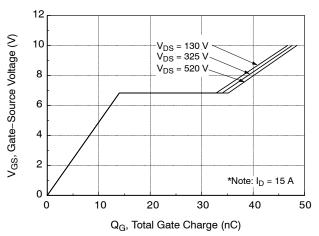
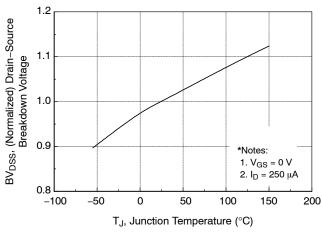


Figure 6. Gate Charge Characteristics

TYPICAL PERFORMANCE CHARACTERISTICS (Continued)

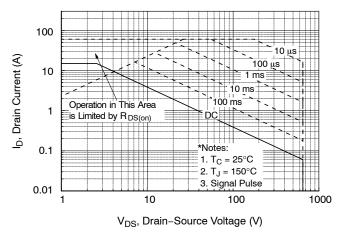
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3.0 R_{DS(ON)}, (Normalized) Drain-Source 2.5 On-Resistance 2.0 1.5 1.0 *Notes: 0.5 1. V_{GS} = 10 V 2. I_D = 7.5 A 0.0 ____ 100 150 200 T_J, Junction Temperature (°C)

Figure 7. Breakdown Voltage Variation vs.
Temperature

Figure 8. On-Resistance Variation vs. Temperature



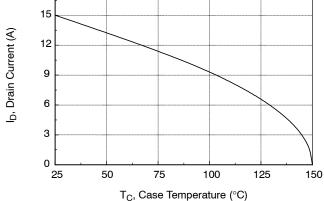


Figure 9. Safe Operating Area

Figure 10. Maximum Drain Current vs. Case Temperature

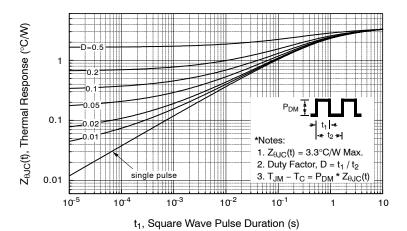


Figure 11. Transient Thermal Response Curve

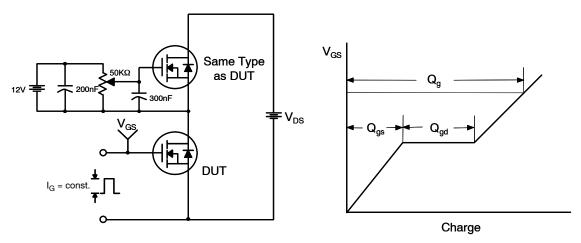


Figure 12. Gate Charge Test Circuit & Waveform

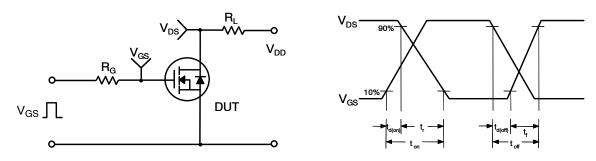


Figure 13. Resistive Switching Test Circuit & Waveforms

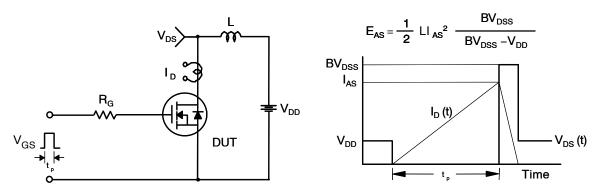
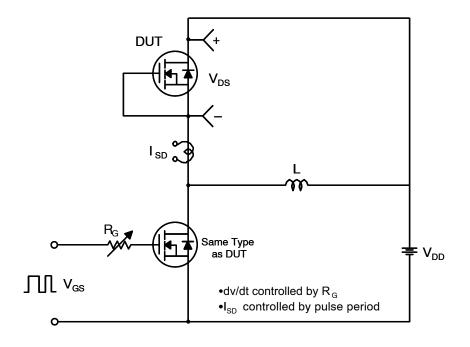


Figure 14. Unclamped Inductive Switching Test Circuit & Waveforms



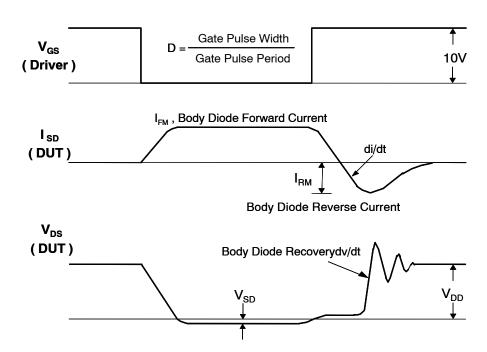
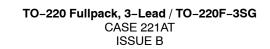


Figure 15. Peak Diode Recovery dv/dt Test Circuit & Waveforms

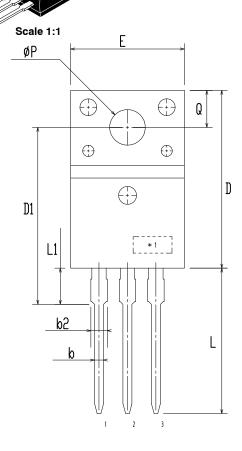
PACKAGE MARKING AND ORDERING INFORMATION

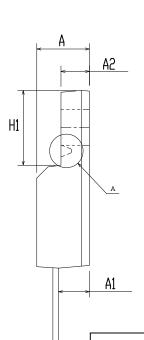
Part Number	Top Mark	Package	Shipping
FDPF15N65	FDPF15N65	TO-220 Fullpack, 3-Lead / TO-220F-3SG CASE 221AT	1,000 Units / Tube

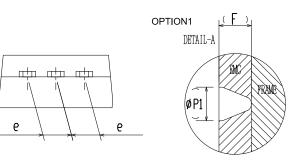
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DATE 19 JAN 2021







DIM	MIL	LIMITERS	
DIM	MIN	NDM	MAX
Α	4.50	4.70	4.90
A1	2.56	2.76	2.96
A2	2.34	2.54	2.74
b	0.70	0.80	0.90
b2	*	2	1.47
C	0.45	0.50	0.60
D	15.67	15.87	16.07
D1	15.60	15.80	16.00
E	9.96	10.16	10.36
е	2.34	2.54	2.74
F	~	0.84	~
H1	6.48	6.68	6.88
Ш	12.78	12.98	13.18
L1	3.03	3.23	3.43
ØΡ	2.98	3.18	3.38
Ø P1	~	1.00	~
Q	3.20	3.30	3.40

NOTES:

- A. DIMENSION AND TOLERANCE AS ASME Y14.5-2009
- B. DIMENSIONS ARE EXCLUSIVE OF BURRS, MOLD FLASH AND TIE BAR PROTRUCSIONS.

C

C. OPTION 1 - WITH SUPPORT PIN HOLE

OPTION 2 - NO SUPPORT PIN HOLE

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DESCRIPTION: TO-220 FULLPACK, 3-LEAD / TO-220F-3SG PAGE 1 OF 1

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