

MOSFET – N-Channel, UniFET™, FRFET®

500 V, 20 A, 260 mΩ

FDP20N50F / FDPF20N50FT

Description

UniFET MOSFET is **onsemi**'s high voltage MOSFET family based on planar stripe and DMOS technology. This MOSFET is tailored to reduce on–state resistance, and to provide better switching performance and higher avalanche energy strength. The body diode's reverse recovery performance of UniFET FRFET MOSFET has been enhanced by lifetime control. Its t_{rr} is less than 100 ns and the reverse dv/dt immunity is 15 V/ns while normal planar MOSFET's have over 200 ns and 4.5 V/ns respectively. Therefore, it can remove additional component and improve system reliability in certain applications in which the performance of MOSFET's body diode is significant. This device family is suitable for switching power converter applications such as power factor correction (PFC), flat panel display (FPD) TV power, ATX and electronic lamp ballasts.

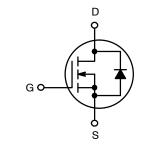
Features

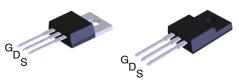
- $R_{DS(on)} = 210 \text{ m}\Omega \text{ (Typ.)} @ V_{GS} = 10 \text{ V}, I_D = 10 \text{ A}$
- Low Gate Charge (Typ. 50 nC)
- Low C_{rss} (Typ. 27 pF)
- 100% Avalanche Tested
- Improve dv/dt Capability
- These Devices are Pb-Free and are RoHS Compliant

Applications

- LCD/LED TV
- Lighting
- Uninterruptible Power Supply
- AC-DC Power Supply

V _{DS}	R _{DS(ON)} MAX	I _D MAX
500 V	260 mΩ @ 10 V	20 A





TO-220-3LD CASE 340AT

TO-220 Fullpack, 3-Lead / TO-220F-3SG CASE 221AT

MARKING DIAGRAM

FDP
20N50F
AYWWZZ

FDPF20 N50FT AYWWZZ

FDP20N50F,

1

FDPF20N50FT = Specific Device Code
A = Assembly Location
YWW = Date Code (Year & Week)
ZZ = Assembly Lot

ORDERING INFORMATION

Device	Package	Shipping
FDP20N50F	TO-220	1000 Units / Tube
FDPF20N50FT	TO-220F	1000 Units / Tube

MOSFET MAXIMUM RATINGS ($T_C = 25^{\circ}C$ unless otherwise noted)

Symbol	Parameter		FDP20N50F	FDPF20N50FT	Unit
V _{DSS}	Drain to Source Voltage	Drain to Source Voltage		500	
V _{GSS}	Gate to Source Voltage		±:	±30	
I _D	Drain Current –	- Continuous ($T_C = 25^{\circ}C$) - Continuous ($T_C = 100^{\circ}C$)	20 12.9	20* 12.9*	Α
I _{DM}	Drain Current	- Pulsed (Note 1)	80	80*	Α
E _{AS}	Single Pulsed Avalanche Energy (Note 2)		1110		mJ
I _{AR}	Avalanche Current (Note 1)		20		Α
E _{AR}	Repetitive Avalanche Energy (Note 1)		25		mJ
dv/dt	Peak Diode Recovery dv/dt (Note 3)		20		V/ns
P _D	Power Dissipation	(T _C = 25°C) – Derate Above 25°C	250 2.0	38.5 0.3	W W/°C
T _J , T _{STG}	Operating and Storage Temperature Range		-55 to +150		°C
T _L	Maximum Lead Temperature for Soldering, 1/8" from Case for 5 Second		300		°C

Stresses exceeding those listed in the Maximum Ratings table may damage the device. If any of these limits are exceeded, device functionality should not be assumed, damage may occur and reliability may be affected. *Drain current limited by maximum junction temperature

1. Repetitive Rating: Pulse width limited by maximum junction temperature.
2. L = 5 mH, I_{AS} = 20 A, V_{DD} = 50 V, R_{G} = 25 Ω , Starting T_{J} = 25°C
3. $I_{SD} \le 20$ A, $I_{SD} \le 20$ A, $I_{SD} \le 8$ BVDSS, Starting $I_{SD} \le 8$ Startin

THERMAL CHARACTERISTICS

Symbol	Parameter	FDP20N50F	FDPF20N50FT	Unit
Rejc	Thermal Resistance, Junction-to-Case, Max.	0.5	3.3	°C/W
Recs	Thermal Resistance, Case-to-sink, Typ.	0.5	-	°C/W
RеJA	Thermal Resistance, Junction-to-Ambient, Max.	62.5	62.5	°C/W

ELECTRICAL CHARACTERISTICS (T_J = 25°C unless otherwise noted)

Parameter	Symbol	Test Condition	Min	Тур	Max	Unit
OFF CHARACTERISTICS						
BV _{DSS}	Drain-Source Breakdown Voltage	$I_D = 250 \mu A, V_{GS} = 0 V, T_J = 25^{\circ}C$	500	_	-	V
$\Delta BV_{DSS}/\Delta T_{J}$	Breakdown Voltage Temperature Coefficient	I _D = 250 μA, Referenced to 25°C	-	0.7	_	V/°C
I _{DSS}	Zero Gate Voltage Drain Current	V _{DS} = 500 V, V _{GS} = 0 V	_	_	10	μΑ
		V _{DS} = 400 V, T _C = 125°C	-	_	100	
I _{GSS}	Gate-Body Leakage Current	$V_{GS} = \pm 30 \text{ V}, V_{DS} = 0 \text{ V}$	_	_	±100	nA
ON CHARACT	ERISTICS					
V _{GS(th)}	Gate Threshold Voltage	$V_{GS} = V_{DS}, I_D = 250 \mu A$	3.0	_	5.0	V
R _{DS(on)}	Static Drain-Source On Resistance	V _{GS} = 10 V, I _D = 10 A	-	0.22	0.26	Ω
9 _{FS}	Forward Transconductance	V _{DS} = 20 V, I _D = 10 A	_	25	_	S
DYNAMIC CHA	ARACTERISTICS					
C _{iss}	Input Capacitance	V _{DS} = 25 V, V _{GS} = 0 V, f = 1 MHz	_	2550	3390	pF
C _{oss}	Output Capacitance		_	350	465	pF
C _{rss}	Reverse Transfer Capacitance	7	_	27	40	pF
Q _{g(tot)}	Total Gate Charge at 10 V	V _{DS} = 400 V, I _D = 20 A,	_	50	65	nC
Q _{gs}	Gate to Source Gate Charge	V _{GS} = 10 V (Note 4)	_	14	_	nC
Q _{gd}	Gate to Drain "Miller" Charge		_	20	-	nC

ELECTRICAL CHARACTERISTICS (T_J = 25°C unless otherwise noted) (continued)

Parameter	Symbol	Test Condition	Min	Тур	Max	Unit
SWITCHING C	CHARACTERISTICS					
t _{d(on)}	Turn-On Delay Time	V _{DD} = 250 V, I _D = 20 A,	-	45	100	ns
t _r	Turn-On Rise Time	$R_G = 25 \Omega$ (Note 4)	-	120	250	ns
t _{d(off)}	Turn-Off Delay Time		_	100	210	ns
t _f	Turn-Off Fall Time		-	60	130	ns
RAIN-SOUR	ICE DIODE CHARACTERISTICS					
IS	Maximum Continuous Drain to Source Diode Forward Current		-	_	20	Α
I _{SM}	Maximum Pulsed Drain to Source Diode Forward Current		-	_	80	Α
V_{SD}	Drain to Source Diode Forward Voltage	V _{GS} = 0 V, I _{SD} = 20 A	-	-	1.5	V
t _{rr}	Reverse Recovery Time	V _{GS} = 0 V, I _{SD} = 20 A	-	154	-	ns
Q _{rr}	Reverse Recovery Charge	dl _F /dt = 100 A/μs	_	0.5	_	μС

Product parametric performance is indicated in the Electrical Characteristics for the listed test conditions, unless otherwise noted. Product performance may not be indicated by the Electrical Characteristics if operated under different conditions.

^{4.} Essentially Independent of Operating Temperature Typical Characteristics

TYPICAL PERFORMANCE CHARACTERISTICS

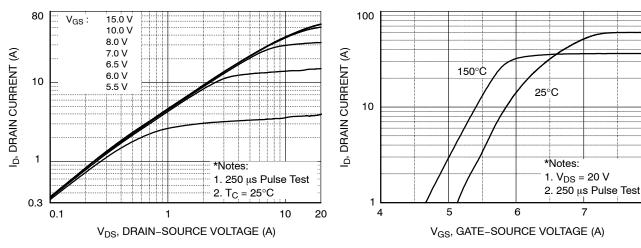


Figure 1. On-Region Characteristics

Figure 2. Transfer Characteristics

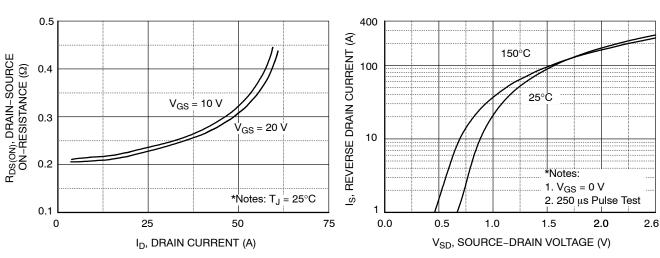


Figure 3. On-Resistance Variation vs. Drain Current and Gate Voltage

Figure 4. Body Diode Forward Voltage Variation vs. Source Current and Temperature

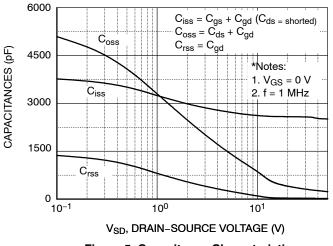


Figure 5. Capacitance Characteristics

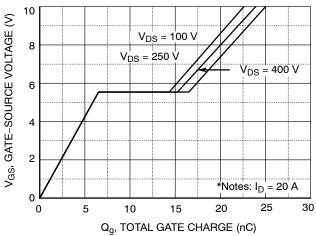


Figure 6. Gate Charge Characteristics

TYPICAL CHARACTERISTICS (continued)

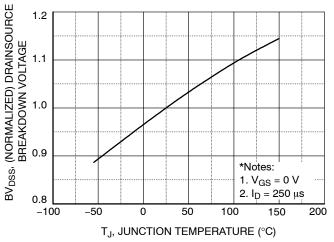


Figure 7. Breakdown Voltage Variation vs. Temperature

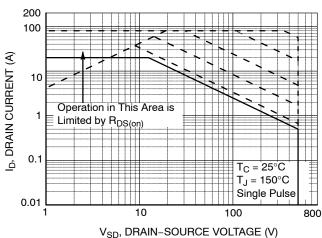


Figure 8. Maximum Safe Operating Area – FDP20N50F

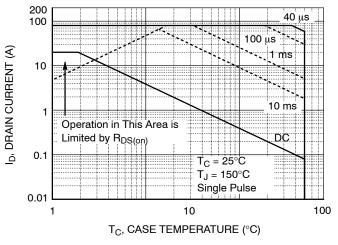


Figure 9. Maximum Safe Operating Area – FDP20N50F

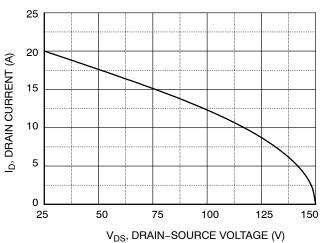


Figure 10. Maximum Drain Current vs. Case Temperature

Typical Performance Characteristics (continued)

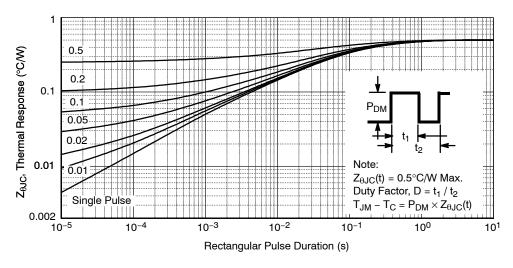


Figure 11. Transient Thermal Response Curve for FDP20N50F

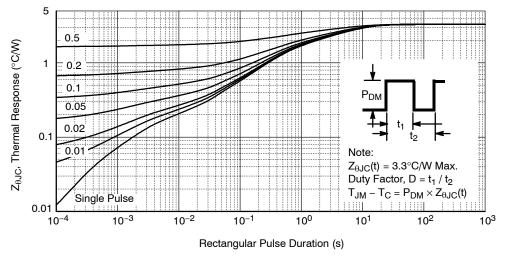


Figure 12. Transient Thermal Response Curve for FDPF20N50FT

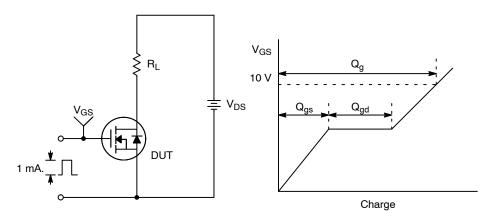


Figure 13. Gate Charge Test Circuit & Waveform

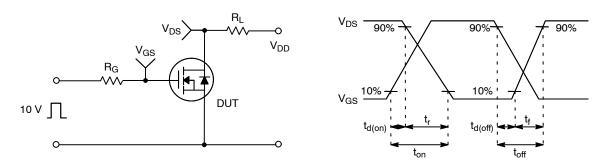


Figure 14. Resistive Switching Test Circuit & Waveforms

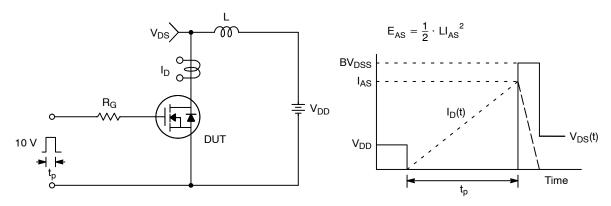


Figure 15. Unclamped Inductive Switching Test Circuit & Waveforms

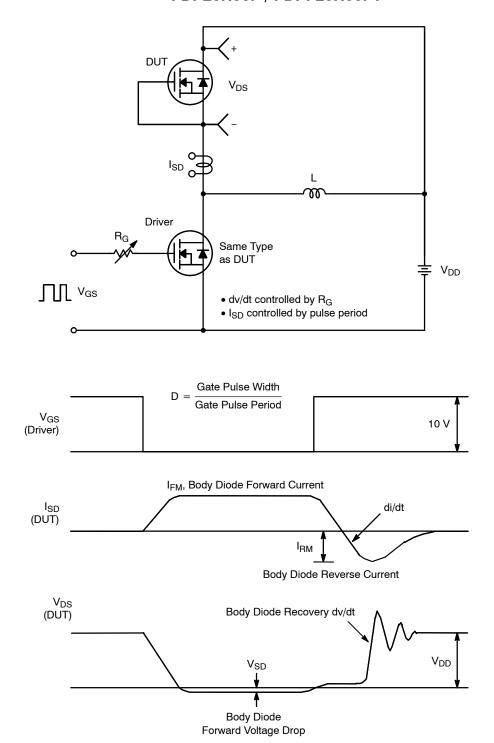
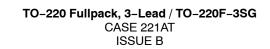


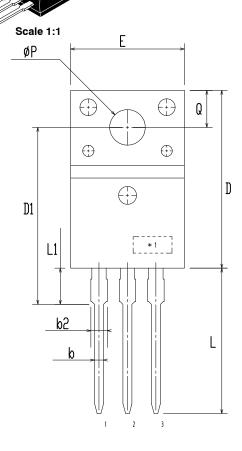
Figure 16. Peak Diode Recovery dv/dt Test Circuit & Waveforms

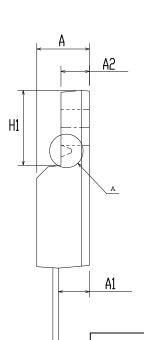
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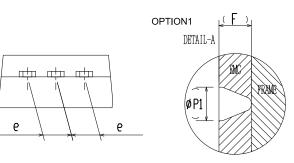
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DATE 19 JAN 2021







DIM	MIL	LIMITERS	
DIM	MIN	NDM	MAX
Α	4.50	4.70	4.90
A1	2.56	2.76	2.96
A2	2.34	2.54	2.74
b	0.70	0.80	0.90
b2	*	2	1.47
C	0.45	0.50	0.60
D	15.67	15.87	16.07
D1	15.60	15.80	16.00
E	9.96	10.16	10.36
е	2.34	2.54	2.74
F	~	0.84	~
H1	6.48	6.68	6.88
Ш	12.78	12.98	13.18
L1	3.03	3.23	3.43
ØΡ	2.98	3.18	3.38
Ø P1	~	1.00	~
Q	3.20	3.30	3.40

NOTES:

- A. DIMENSION AND TOLERANCE AS ASME Y14.5-2009
- B. DIMENSIONS ARE EXCLUSIVE OF BURRS, MOLD FLASH AND TIE BAR PROTRUCSIONS.

C

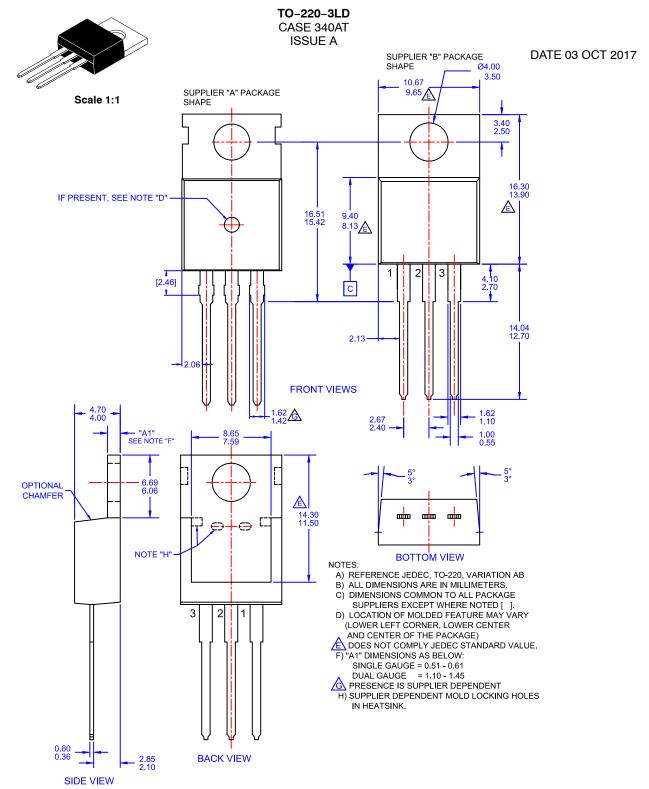
C. OPTION 1 - WITH SUPPORT PIN HOLE

OPTION 2 - NO SUPPORT PIN HOLE

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