Thank you for your interest in **onsemi** products.

Your technical document begins on the following pages.



Your Feedback is Important to Us!

Please take a moment to participate in our short survey. At **onsemi**, we are dedicated to delivering technical content that best meets your needs.

Help Us Improve - Take the Survey

This survey is intended to collect your feedback, capture any issues you may encounter, and to provide improvements you would like to suggest.

We look forward to your feedback.

To learn more about **onsemi**, please visit our website at <u>www.onsemi.com</u>

onsemi and ONSEMI. and other names, marks, and brands are registered and/or common law trademarks of Semiconductor Components Industries, LLC dba "onsemi" or its affiliates and/or subsidiaries in the United States and/or other countries. onsemi owns the rights to a number of patents, trademarks, copyrights, trade secrets, and other intellectual property. A listing of onsemi product/patent coverage may be accessed at www.onsemi.com/site/pdf/Patent-Marking.pdf. onsemi reserves the right to make changes at any time to any products or information herein, without or suitability of its products for any particular purpose, nor does onsemi assume any liability arising out of the application or use of any product or circuit, and specifically disclaims any and all liability, including without limitation special, consequential or incidental damages. Buyer is responsible for its products and applications using onsemi products, including compliance with all laws, regulations and safety requirements or standards, regardless of any support or applications information provided by onsemi. "Typical" parameters which may be provided in onsemi data sheets and/or specifications can and do vary in different applications and actual performance may vary over time. All opreating parameters, including "Typicals" must be validated for each customer application in the human body. Should Buyer purchase or use onsemi products for any such unintended or unauthorized application, Buyer shall indemnify and hold onsemi and its officers, employees, subsidiaries, affiliates, and distributors harmless against all claims, costs, damages, and ereasnable attorney fees arising out of, directly or indirectly, any claim of personal injury or death Action Employer. This literature is subject to all applicatione claimed as not for resale in any manner. Other names and brands may be claimed as the property of others.

Onsemi

TinyLogic UHS Triple Inverter

NC7NZ04

Desription

The NC7NZ04 is a triple inverter from **onsemi**'s Ultra–High Speed (UHS) series of TinyLogic. The device is fabricated with advanced CMOS technology to achieve ultra-high speed with high output drive while maintaining low static power dissipation over a broad V_{CC} operating range. The device is specified to operate over the 1.65 V to 5.5 V V_{CC} operating range. The inputs and output are high impedance when V_{CC} is 0 V. Inputs tolerate voltages up to 5.5 V, independent of V_{CC} operating voltage.

Features

- Ultra-High Speed: $t_{PD} = 2.4$ ns (Typical) into 50 pF at 5 V V_{CC}
- High Output Drive: ±24 mA at 3 V V_{CC}
- Broad V_{CC} Operating Range: 1.65 V to 5.5 V Power–Down, High-Impedance Inputs / Outputs
- Over-Voltage Tolerance Inputs Facilitate 5 V to 3 V Translation
- Proprietary Noise / EMI Reduction Circuitry
- Space-Saving MicroPak[™] and US8 Surface Mount Packages
- These Devices are Pb-Free, Halogen Free/BFR Free and are RoHS Compliant

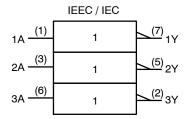
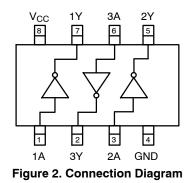
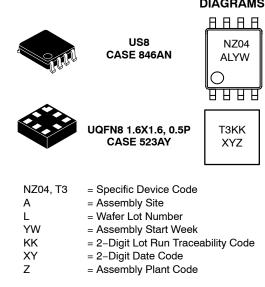


Figure 1. Logic Symbol

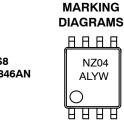




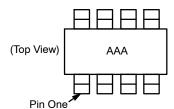
ORDERING INFORMATION

See detailed ordering, marking and shipping information on page 5 of this data sheet.

NOTE: Some of the devices on this data sheet have been DISCONTINUED. Please refer to the table on page 5.



Pin Configurations



NOTES:

- 1. AAA represents product code top mark (see ordering table).
- Orientation of top mark determines pin one location. Reading the top product code mark left to right, pin one is the lower left pin.

Figure 3. US8

PIN DEFINITIONS

Pin # US8	Pin # MicroPak	Name	Description
1	7	1A	Input
2	6	ЗY	Output
3	5	2A	Input
4	4	GND	Ground
5	3	2Y	Output
6	2	ЗA	Input
7	1	1Y	Output
8	8	V _{CC}	Supply Voltage

Figure 4. MicroPak (Top Through View)

FUNCTION TABLE

Inputs	Output
Α	Y
L	Н
Н	L

ABSOLUTE MAXIMUM RATINGS

Symbol	Parame	ter	Min	Мах	Unit
V _{CC}	Supply Voltage		-0.5	6.5	V
V _{IN}	DC Input Voltage		-0.5	6.5	V
V _{OUT}	DC Output Voltage		-0.5	6.5	V
I _{IK}	DC Input Diode Current	V _{IN} < 0 V	-	-50	mA
Ι _{ΟΚ}	DC Output Diode Current	V _{OUT} < 0 V	-	-50	mA
I _{OUT}	DC Output Current	-	±50	mA	
$I_{CC} \text{ or } I_{GND}$	DC V _{CC} or Ground Current	-	±50	mA	
T _{STG}	Storage Temperature Range	-65	+150	°C	
TJ	Junction Temperature Under Bias		-	+150	°C
ΤL	Junction Lead Temperature (Solder	ring, 10 Seconds)	-	+260	°C
PD	Power Dissipation in Still Air	US8	-	500	mW
		MicroPak-8	-	539	
ESD	Human Body Model, JEDEC: JESD22-A114		-	4000	V
	Charge Device Model, JEDEC: JES	SD22-C101	-	2000	

Stresses exceeding those listed in the Maximum Ratings table may damage the device. If any of these limits are exceeded, device functionality should not be assumed, damage may occur and reliability may be affected.

NC7NZ04

RECOMMENDED OPERATING CONDITIONS

Symbol	Parameter	Conditions	Min	Max	Unit
V _{CC}	Supply Voltage Operating		1.65	5.5	V
	Supply Voltage Data Retention		1.5	5.5	
V _{IN}	Input Voltage		0	5.5	V
V _{OUT}	Output Voltage		0	V _{CC}	V
T _A	Operating Temperature		-40	+85	°C
t _r , t _f	Input Rise and Fall Times	V_{CC} at 1.8 V, 2.5 V ± 0.2 V	0	20	ns/V
		V_{CC} at 3.3 V ± 0.3 V	0	10	
		V_{CC} at 5.0 V ± 0.5 V	0	5	
θ_{JA}	Thermal Resistance	US8	-	250	°C/W
		MicroPak-8	-	232	

Functional operation above the stresses listed in the Recommended Operating Ranges is not implied. Extended exposure to stresses beyond the Recommended Operating Ranges limits may affect device reliability. 3. Unused inputs must be held HIGH or LOW. They may not float.

DC ELECTICAL CHARACTERISTICS

				Т	A = 25°	C	$T_{A} = -40$		
Symbol	Parameter	Vcc	Conditions	Min	Тур	Max	Min	Max	Unit
V _{IH}	HIGH Level Input Voltage	1.65 to 1.95		0.65 V _{CC}	_	-	0.65 V _{CC}	-	V
		2.30 to 5.50		0.70 V _{CC}	_	-	0.70 V _{CC}	-	
V _{IL}	LOW Level Input Voltage	1.65 to 1.95		-	_	0.35 V _{CC}	-	0.35 V _{CC}	V
		2.30 to 5.50		-	_	0.30 V _{CC}	-	0.30 V _{CC}	
V _{OH}	HIGH Level Output	1.65	$V_{IN} = V_{IH} \text{ or } V_{IL},$	1.55	1.65	-	1.55	-	V
	Voltage	2.30 lo	· I _{OH} = –100 μΑ	2.20	2.30	-	2.20	-	
		3.00		2.90	3.00	-	2.90	-	
		4.50		4.40	4.50	-	4.40	-	
		1.65	I _{OH} = -4 mA	1.29	1.52	-	1.29	-	
		2.30	I _{OH} = -8 mA	1.90	2.15	-	1.90	-	
		3.00	I _{OH} = -16 mA	2.40	2.80	-	2.40	-	
		3.00	I _{OH} = -24 mA	2.30	2.68	-	2.30	-	
		4.50	I _{OH} = -32 mA	3.80	4.20	-	3.80	-	
V _{OL}	LOW Level Output	1.65	$V_{IN} = V_{IH} \text{ or } V_{IL},$	-	0.00	0.10	-	0.10	V
	Voltage	2.30	I _{OH} = -100 μA	-	0.00	0.10	-	0.10	
		3.00		-	0.00	0.10	-	0.10	
		4.50		-	0.00	0.10	-	0.10	
		1.65	I _{OH} = 4 mA	-	0.80	0.24	-	0.24	
		2.30	I _{OH} = 8 mA	-	0.10	0.30	-	0.30	
		3.00	I _{OH} = 16 mA	-	0.15	0.40	-	0.40	
		3.00	I _{OH} = 24 mA	-	0.22	0.55	-	0.55	
		4.50	I _{OH} = 32 mA	-	0.22	0.55	-	0.55	
I _{IN}	Input Leakage Current	1.65 to 5.5	V _{IN} = 5.5 V, GND	-	-	±1	-	±1	μA
I _{OFF}	Power-Off Leakage Current	0	V_{IN} or V_{OUT} = 5.5 V	-	-	1	-	10	μA
I _{CC}	Quiescent Supply Current	1.65 to 5.50	V _{IN} = 5.5 V, GND	_	-	1	_	10	μA

NC7NZ04

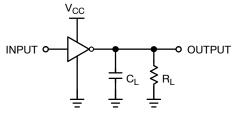
AC ELECTRICAL CHARACTERISTICS

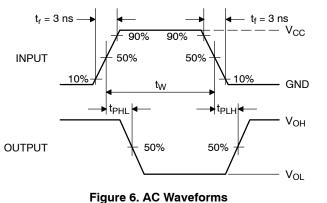
				T _A = 25°C			$T_{A} = -40$) to 85°C	
Symbol	Parameter	V _{cc}	Conditions	Min	Тур	Max	Min	Max	Unit
t _{PLH} , t _{PHL}	Propagation Delay	1.80 ±0.15	C _L = 15 pF,	_	4.4	9.5	-	10.0	ns
	(Figure 5, 6)	2.50 ±0.20	$R_L = 1 M\Omega$	-	2.9	5.1	-	5.6	
		$3.30\pm\!\!0.30$	0	-	2.1	3.4	-	3.8	
		5.00 ±0.50		-	1.8	2.8	-	3.1	
		3.30 ±0.30	$C_{L} = 50 \text{ pF},$	_	2.9	4.5	-	5.0	
		5.00 ±0.50	R _L = 500 Ω	_	2.4	3.6	-	4.0	
C _{IN}	Input Capacitance	0		_	2.5	-	-	-	pF
C _{PD}		3.30		_	9	-	-	-	pF
	(Note 4) (Figure 7)	5.00		-	11	-	-	_	

4. C_{PD} is defined as the value of the internal equivalent capacitance which is derived from dynamic operating current consumption (I_{CCD}) at no output lading and operating at 50% duty cycle. C_{PD} is related to I_{CCD} dynamic operating current by the expression: I_{CCD} = (C_{PD}) (V_{CC}) (f_{IN}) + (I_{CC}static).

DYNAMIC SWITCHING CHARACTERISTICS

				T _A = 25°C	
Symbol	Parameter	Conditions	V _{cc}	Тур	Unit
V _{OLP}	Quiet Output Dynamic Peak V _{OL}	C_L = 50 pF, V_{IH} = 5.0 V, V_{IL} = 0 V	5.0	0.8	V
V _{OLV}	Quiet Output Dynamic Valley V_{OL}		5.0	-0.8	V

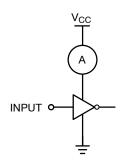




NOTE:

5. C_{L} includes load and stray capacitance; inputs PRR = 1.0 MHz, t_{W} = 500 ns.





NOTE:

Figure 7. I_{CCD} Test Circuit

NC7NZ04

DEVICE ORDERING INFORMATION

Device	Top Mark	Packages	Shipping [†]
NC7NZ04K8X	NZ04	8-Lead US8, JEDEC MO-187, Variation CA 3.1 mm Wide	3000 / Tape & Reel
NC7NZ04L8X	T3	8-Lead MicroPak, 1.6 mm Wide	5000 / Tape & Reel

DISCONTINUED (Note 7)

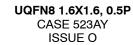
NC7NZ04K8X-L22236	NZ04	8-Lead US8, JEDEC MO-187, Variation CA 3.1 mm Wide	3000 / Tape & Reel
NC7NZ04L8X-L22185	T3	8-Lead MicroPak, 1.6 mm Wide	5000 / Tape & Reel

+For information on tape and reel specifications, including part orientation and tape sizes, please refer to our Tape and Reel Packaging Specifications Brochure, BRD8011/D.

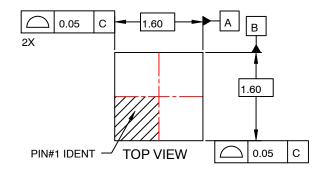
7. **DISCONTINUED:** These devices are not recommended for new design. Please contact your **onsemi** representative for information. The most current information on these devices may be available on <u>www.onsemi.com</u>.

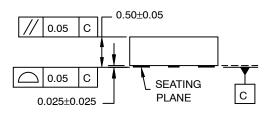
MicroPak is trademark of Semiconductor Components Industries, LLC dba "onsemi" or its affiliates and/or subsidiaries in the United States and/or other countries.



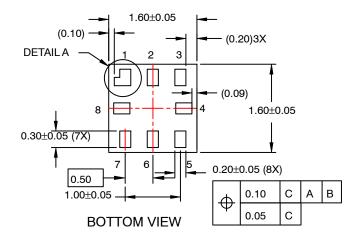


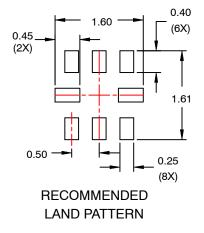
DATE 31 AUG 2016





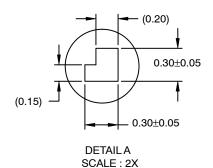
SIDE VIEW





NOTES:

- A. PACKAGE CONFORMS TO JEDEC MO-255 VARIATION UAAD.
- B. DIMENSIONS ARE IN MILLIMETERS.
- C. DIMENSIONS AND TOLERANCES PER ASME Y14.5M, 2009.
- D. LAND PATTERN RECOMMENDATION IS EXISTING INDUSTRY LAND PATTERN.



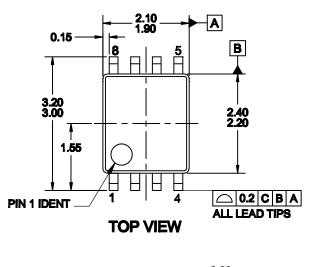
DOCUMENT NUMBER:	98AON13591G	Electronic versions are uncontrolled except when accessed directly from the Document Reposit Printed versions are uncontrolled except when stamped "CONTROLLED COPY" in red.					
DESCRIPTION:	UQFN8 1.6X1.6, 0.5P		PAGE 1 OF 1				
ON Semiconductor reserves the right the suitability of its products for any pa	ON Semiconductor and I are trademarks of Semiconductor Components Industries, LLC dba ON Semiconductor or its subsidiaries in the United States and/or other countries. ON Semiconductor reserves the right to make changes without further notice to any products herein. ON Semiconductor makes no warranty, representation or guarantee regarding the suitability of its products for any particular purpose, nor does ON Semiconductor assume any liability arising out of the application or use of any product or circuit, and specifically disclaims any and all liability, including without limitation special, consequential or incidental damages. ON Semiconductor does not convey any license under its patent rights nor the						

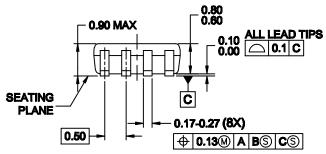
© Semiconductor Components Industries, LLC, 2019



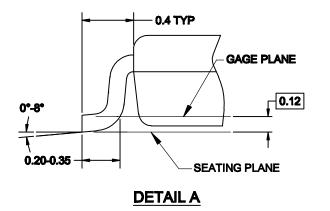
US8 CASE 846AN ISSUE O

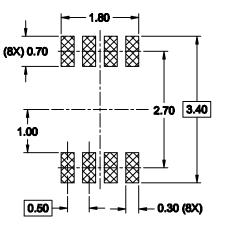
DATE 31 DEC 2016







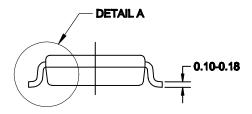




RECOMMENDED LAND PATTERN

NOTES:

- A. CONFORMS TO JEDEC REGISTRATION MO-187
- **B. DIMENSIONS ARE IN MILLIMETERS.**
- C. DIMENSIONS ARE EXCLUSIVE OF BURRS, MOLD FLASH, AND TIE BAR EXTRUSIONS.
- D. DIMENSIONS AND TOLERANCES PER ANSI Y14.5M, 1994.



DOCUMENT NUMBER:	98AON13778G	AON13778G Electronic versions are uncontrolled except when accessed directly from the Document Re Printed versions are uncontrolled except when stamped "CONTROLLED COPY" in red.			
DESCRIPTION:	US8		PAGE 1 OF 1		
ON Semiconductor reserves the right the suitability of its products for any pa	to make changes without further notice to an articular purpose, nor does ON Semiconducto	stries, LLC dba ON Semiconductor or its subsidiaries in the United States y products herein. ON Semiconductor makes no waranty, representation r assume any liability arising out of the application or use of any product or cidental damages. ON Semiconductor does not convey any license under	or guarantee regarding circuit, and specifically		

© Semiconductor Components Industries, LLC, 2019

onsemi, ONSEMI, and other names, marks, and brands are registered and/or common law trademarks of Semiconductor Components Industries, LLC dba "onsemi" or its affiliates and/or subsidiaries in the United States and/or other countries. onsemi owns the rights to a number of patents, trademarks, copyrights, trade secrets, and other intellectual property. A listing of onsemi's product/patent coverage may be accessed at <u>www.onsemi.com/site/pdf/Patent_Marking.pdf</u>. onsemi reserves the right to make changes at any time to any products or information herein, without notice. The information herein is provided "as-is" and onsemi makes no warranty, representation or guarantee regarding the accuracy of the information, product features, availability, functionality, or suitability of its products for any particular purpose, nor does onsemi assume any liability arising out of the application or use of any product or circuit, and specifically disclaims any and all liability, including without limitation special, consequential or indental damages. Buyer is responsible for its products and applications using onsemi products, including compliance with all laws, regulations and safety requirements or standards, regardless of any support or applications information provided by onsemi. "Typical" parameters which may be provided in onsemi data sheets and/or specifications can and do vary in different applications and actual performance may vary over time. All operating parameters, including "Typicals" must be validated for each customer application by customer's technical experts. onsemi does not convey any license under any of its intellectual property rights nor the rights of others. onsemi products are not designed, intended, or authorized for use as a critical component in life support systems or any FDA Class 3 medical devices or medical devices with a same or similar classification. Buyer shall indemnify and hold onsemi and its officers, employees, subsidiaries, affiliates, and distributors harmless against all claims, costs,

ADDITIONAL INFORMATION

TECHNICAL PUBLICATIONS:

Technical Library: www.onsemi.com/design/resources/technical-documentation onsemi Website: www.onsemi.com

ONLINE SUPPORT: <u>www.onsemi.com/support</u> For additional information, please contact your local Sales Representative at <u>www.onsemi.com/support/sales</u>