# Power MOSFET, N-Channel, SUPERFET<sup>®</sup> III, FRFET<sup>®</sup>, 650 V, 75 A, 27.4 m $\Omega$

#### Description

SUPERFET III MOSFET is ON Semiconductor's brand-new high voltage super-junction (SJ) MOSFET family that is utilizing charge balance technology for outstanding low on-resistance and lower gate charge performance. This advanced technology is tailored to minimize conduction loss, provide superior switching performance, and withstand extreme dv/dt rate.

Consequently, SUPERFET III MOSFET is very suitable for the various power system for miniaturization and higher efficiency.

SUPERFET III FRFET MOSFET's optimized reverse recovery performance of body diode can remove additional component and improve system reliability.

#### Features

- 700 V @ T<sub>J</sub> = 150°C
- Typ.  $R_{DS(on)} = 23 \text{ m}\Omega$
- Ultra Low Gate Charge (Typ. Q<sub>g</sub> = 225 nC)
- Low Effective Output Capacitance (Typ. Coss(eff.) = 1878 pF)
- 100% Avalanche Tested
- These Devices are Pb-Free and are RoHS Compliant

#### Applications

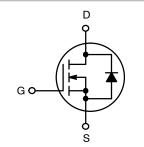
- Telecom / Server Power Supplies
- Industrial Power Supplies
- EV Charger
- UPS / Solar



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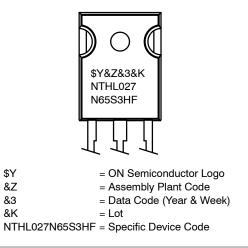
V <sub>DSS</sub>	R <sub>DS(ON)</sub> MAX	I <sub>D</sub> MAX
650 V	27.4 m $\Omega$ @ 10 V	75 A



POWER MOSFET



#### **MARKING DIAGRAM**



### ORDERING INFORMATION

See detailed ordering and shipping information on page 2 of this data sheet.

Symbol	Parameter		Value	Unit
V <sub>DSS</sub>	Drain to Source Voltage		650	V
V <sub>GSS</sub>	Gate to Source Voltage – DC		±30	V
		– AC (f > 1 Hz)	±30	
I <sub>D</sub>	Drain Current	– Continuous ( $T_C = 25^{\circ}C$ )	75	А
		– Continuous (T <sub>C</sub> = 100°C)	60	
I <sub>DM</sub>	Drain Current	– Pulsed (Note 1)	187.5	А
E <sub>AS</sub>	Single Pulsed Avalanche Energy (Note 2)		1610	mJ
I <sub>AS</sub>	Avalanche Current (Note 2)		15	А
E <sub>AR</sub>	Repetitive Avalanche Energy (Note 1)		5.95	mJ
dv/dt	MOSFET dv/dt		100	V/ns
	Peak Diode Recovery dv/dt (Note 3)		50	
PD	Power Dissipation	(T <sub>C</sub> = 25°C)	595	W
		– Derate Above 25°C	4.76	W/°C
T <sub>J</sub> , T <sub>STG</sub>	Operating and Storage Temperature Range		-55 to +150	°C
ΤL	Maximum Lead Temperature for Soldering, 1/8	8" from Case for 5 seconds	300	°C

#### ABSOLUTE MAXIMUM RATINGS (T<sub>C</sub> = 25°C, Unless otherwise noted)

Stresses exceeding those listed in the Maximum Ratings table may damage the device. If any of these limits are exceeded, device functionality should not be assumed, damage may occur and reliability may be affected. 1. Repetitive rating: pulse-width limited by maximum junction temperature. 2.  $I_{AS} = 15 \text{ A}, \text{ R}_{G} = 25 \Omega$ , starting  $T_{J} = 25^{\circ}\text{C}$ . 3.  $I_{SD} \leq 37.5 \text{ A}, \text{ di/dt} \leq 200 \text{ A/}\mu\text{s}, \text{V}_{DD} \leq 400 \text{ V}$ , starting  $T_{J} = 25^{\circ}\text{C}$ .

#### **THERMAL CHARACTERISTICS**

Symbol	Parameter	Value	Unit
$R_{\theta JC}$	Thermal Resistance, Junction to Case, Max.	0.21	°C/W
$R_{\theta JA}$	Thermal Resistance, Junction to Ambient, Max.	40	

#### PACKAGE MARKING AND ORDERING INFORMATION

Part Number	Top Marking	Package	Packing Method	Reel Size	Tape Width	Quantity
NTHL027N65S3HF	NTHL027N65S3HF	TO-247	Tube	N/A	N/A	30 Units

# **ELECTRICAL CHARACTERISTICS** ( $T_C = 25^{\circ}C$ unless otherwise noted)

Symbol	Parameter	Test Conditions	Min.	Тур.	Max.	Unit
OFF CHARACT	ERISTICS		•			
BV <sub>DSS</sub>	Drain to Source Breakdown Voltage	$V_{GS}$ = 0 V, I <sub>D</sub> = 1 mA, T <sub>J</sub> = 25°C	650			V
		$V_{GS}$ = 0 V, I <sub>D</sub> = 1 mA, T <sub>J</sub> = 150°C	700			V
$\Delta \text{BV}_{\text{DSS}} / \Delta \text{T}_{\text{J}}$	Breakdown Voltage Temperature Coefficient	$I_D = 15 \text{ mA}$ , Referenced to $25^{\circ}\text{C}$		0.61		V/°C
I <sub>DSS</sub>	Zero Gate Voltage Drain Current	$V_{DS} = 650 \text{ V}, \text{ V}_{GS} = 0 \text{ V}$			10	μΑ
		$V_{DS} = 520 \text{ V}, \text{ T}_{C} = 125^{\circ}\text{C}$		361		
I <sub>GSS</sub>	Gate to Body Leakage Current	$V_{GS}=\pm30~\text{V},~\text{V}_{DS}=0~\text{V}$			±100	nA
N CHARACTE	RISTICS					
Vocatio	Gate Threshold Voltage	$V_{CC} = V_{DC}$ $l_{D} = 3 \text{ mA}$	3.0		5.0	V

VGS(th)	Gale Threshold Vollage	$v_{GS} = v_{DS}, i_D = 3 i_{IIA}$	3.0		5.0	v
R <sub>DS(on)</sub>	Static Drain to Source On Resistance	$V_{GS}$ = 10 V, I <sub>D</sub> = 35 A		23	27.4	mΩ
9fs	Forward Transconductance	$V_{DS}$ = 20 V, $I_{D}$ = 37.5 A		56		S

#### DYNAMIC CHARACTERISTICS

C <sub>iss</sub>	Input Capacitance	$V_{DS}$ = 400 V, $V_{GS}$ = 0 V, f = 1 MHz	7630	pF
C <sub>oss</sub>	Output Capacitance		190	pF
C <sub>oss(eff.)</sub>	Effective Output Capacitance	$V_{DS}$ = 0 V to 400 V, $V_{GS}$ = 0 V	1878	pF
C <sub>oss(er.)</sub>	Energy Related Output Capacitance	$V_{DS}$ = 0 V to 400 V, $V_{GS}$ = 0 V	352	pF
Q <sub>g(tot)</sub>	Total Gate Charge at 10 V	$V_{DS} = 400 \text{ V}, \text{ I}_{D} = 37.5 \text{ A}, \text{ V}_{GS} = 10 \text{ V}$	225	nC
Q <sub>gs</sub>	Gate to Source Gate Charge	(Note 4)	67	nC
Q <sub>gd</sub>	Gate to Drain "Miller" Charge	]	88	nC
ESR	Equivalent Series Resistance	f = 1 MHz	1.2	Ω

SWITCHING CHARACTERISTICS

t <sub>d(on)</sub>	Turn-On Delay Time	$V_{DD} = 400 \text{ V}, \text{ I}_{D} = 37.5 \text{ A}, \text{ V}_{GS} = 10 \text{ V}$	47	ns
t <sub>r</sub>	Turn-On Rise Time	R <sub>g</sub> = 2 Ω (Note 4)	38	ns
t <sub>d(off)</sub>	Turn-Off Delay Time		124	ns
t <sub>f</sub>	Turn-Off Fall Time		32	ns

SOURCE-DRAIN DIODE CHARACTERISTICS

۱ <sub>S</sub>	Maximum Continuous Source to Drain Diode Forward Current			75	А
I <sub>SM</sub>	Maximum Pulsed Source to Drain Diode Forward Current			187.5	А
V <sub>SD</sub>	Source to Drain Diode Forward Voltage	$V_{GS} = 0 V, I_{SD} = 37.5 A$		1.3	V
t <sub>rr</sub>	Reverse Recovery Time	$V_{DD} = 400 \text{ V}, I_{SD} = 37.5 \text{ A},$	182		ns
Q <sub>rr</sub>	Reverse Recovery Charge	dl <sub>F</sub> /dt = 100 A/μs	1128		nC

Product parametric performance is indicated in the Electrical Characteristics for the listed test conditions, unless otherwise noted. Product performance may not be indicated by the Electrical Characteristics if operated under different conditions.

4. Essentially independent of operating temperature typical characteristics.

### **TYPICAL PERFORMANCE CHARACTERISTICS**

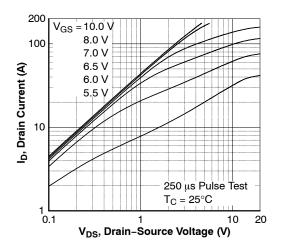
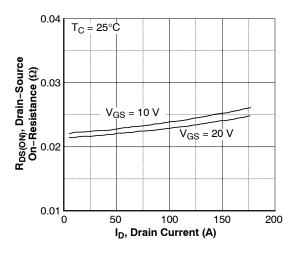
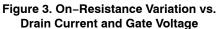


Figure 1. On-Region Characteristics





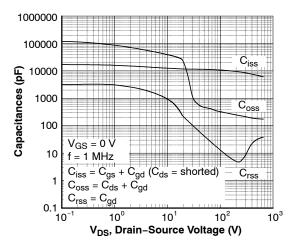


Figure 5. Capacitance Characteristics

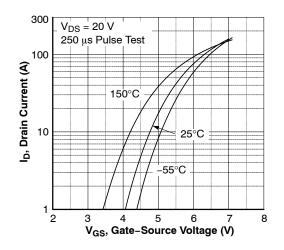


Figure 2. Transfer Characteristics

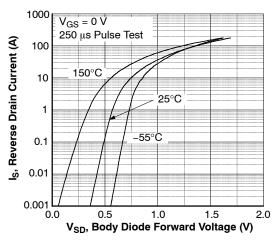


Figure 4. Body Diode Forward Voltage Variation vs. Source Current and Temperature

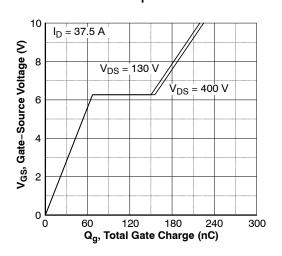
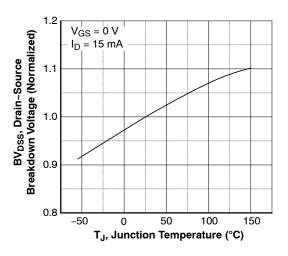


Figure 6. Gate Charge Characteristics

#### TYPICAL PERFORMANCE CHARACTERISTICS (continued)





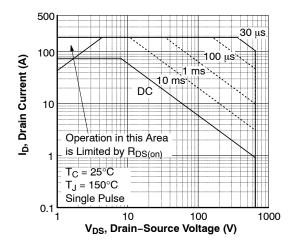


Figure 9. Maximum Safe Operating Area

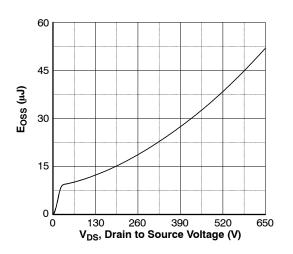


Figure 11. E<sub>OSS</sub> vs. Drain to Source Voltage

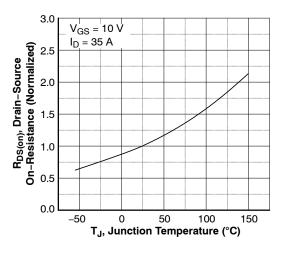


Figure 8. On–Resistance Variation vs. Temperature

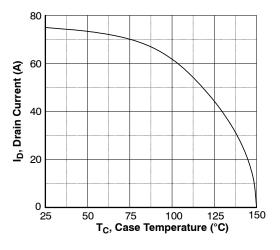


Figure 10. Maximum Drain Current vs. Case Temperature

# TYPICAL PERFORMANCE CHARACTERISTICS (continued)

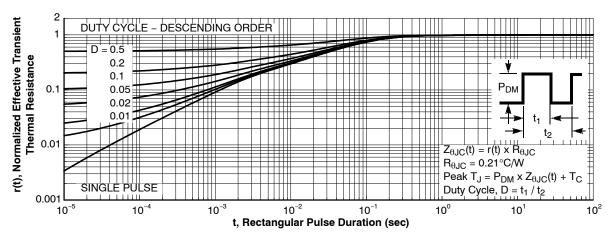
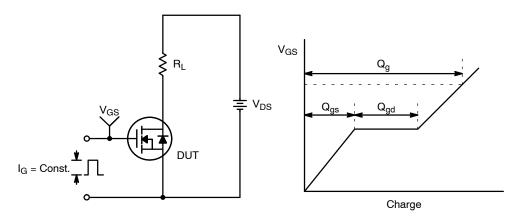


Figure 12. Transient Thermal Response Curve





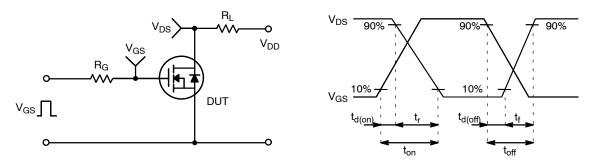
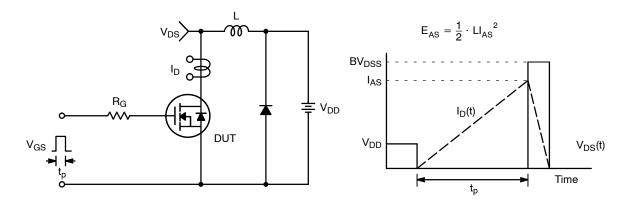


Figure 14. Resistive Switching Test Circuit & Waveforms





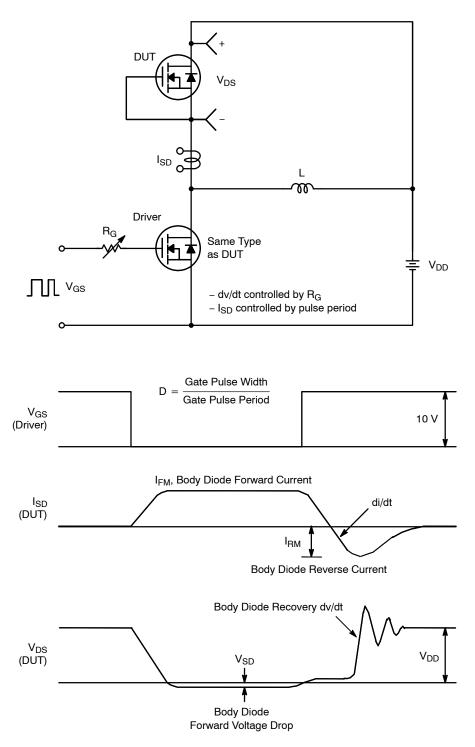
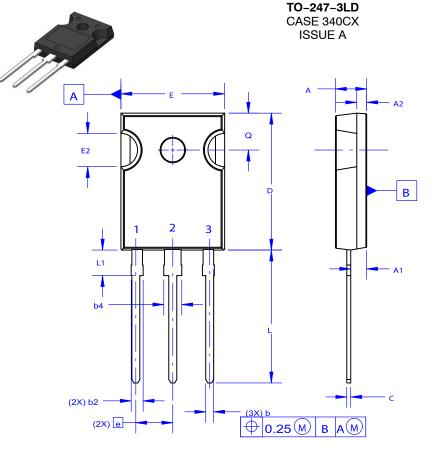


Figure 16. Peak Diode Recovery dv/dt Test Circuit & Waveforms

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NOTES: UNLESS OTHERWISE SPECIFIED.

- A. DIMENSIONS ARE EXCLUSIVE OF BURRS, MOLD FLASH, AND TIE BAR EXTRUSIONS.
- B. ALL DIMENSIONS ARE IN MILLIMETERS.
- C. DRAWING CONFORMS TO ASME Y14.5 2009.
- D. DIMENSION A1 TO BE MEASURED IN THE REGION DEFINED BY L1.
- E. LEAD FINISH IS UNCONTROLLED IN THE REGION DEFINED BY L1.

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# GENERIC **MARKING DIAGRAM\*** Х



XXXXX	= Specific Device Code
Α	= Assembly Location

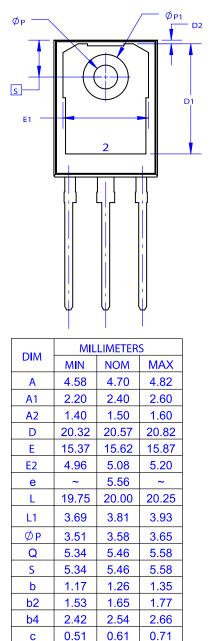
- = Assembly Location
- = Year
- ww = Work Week
- G = Pb-Free Package

\*This information is generic. Please refer to device data sheet for actual part marking. Pb-Free indicator, "G" or microdot " ", may or may not be present. Some products may not follow the Generic Marking.

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