# NVBLS0D7N04M8

# **MOSFET** – Power, Single, N-Channel

# 40 V, 240 A, 0.75 m $\Omega$

#### Features

- Typical  $R_{DS(on)} = 0.59 \text{ m}\Omega$  at  $V_{GS} = 10 \text{ V}$ ,  $I_D = 80 \text{ A}$
- Typical  $Q_{g(tot)} = 144 \text{ nC}$  at  $V_{GS} = 10 \text{ V}$ ,  $I_D = 80 \text{ A}$
- UIS Capability
- AEC-Q101 Qualified and PPAP Capable
- These Devices are Pb-Free and are RoHS Compliant

#### **MAXIMUM RATINGS** $T_J = 25^{\circ}C$ unless otherwise noted

| Parameter   | Symbol                            | Ratings         | Units |
|---|-----------------------------------|-----------------|-------|
| Drain-to-Source Voltage   | V <sub>DSS</sub>                  | 40              | V     |
| Gate-to-Source Voltage  | V <sub>GS</sub>                   | ±20             | V     |
| Drain Current – Continuous (V <sub>GS</sub> = 10)<br>(Note 1) T <sub>C</sub> = 25°C | ۱ <sub>D</sub>                    | 240             | A     |
| Pulsed Drain Current $T_{C} = 25^{\circ}C$  |                                   | See<br>Figure 4 |       |
| Single Pulse Avalanche Energy (Note 2)  | E <sub>AS</sub>                   | 737             | mJ    |
| Power Dissipation   | PD                                | 357             | W     |
| Derate Above 25°C   |                                   | 2.38            | W/°C  |
| Operating and Storage Temperature   | T <sub>J</sub> , T <sub>STG</sub> | –55 to +175     | °C    |
| Thermal Resistance, Junction-to-Case  | $R_{\theta JC}$                   | 0.42            | °C/W  |
| Maximum Thermal Resistance,<br>Junction-to-Ambient (Note 3)                         | $R_{	hetaJA}$                     | 43              | °C/W  |

Stresses exceeding those listed in the Maximum Ratings table may damage the device. If any of these limits are exceeded, device functionality should not be assumed, damage may occur and reliability may be affected.

- 1. Current is limited by bondwire configuration.
- 2. Starting  $T_J = 25^{\circ}C$ , L = 0.36 mH,  $I_{AS} = 64$  A,  $V_{DD} = 40$  V during inductor charging and  $V_{DD} = 0$  V during time in avalanche.
- 3. R<sub>0JA</sub> is the sum of the junction-to-case and case-to-ambient thermal resistance, where the case thermal reference is defined as the solder mounting surface of the drain pins. R<sub>0JC</sub> is guaranteed by design, while R<sub>0JA</sub> is determined by the board design. The maximum rating presented here is based on mounting on a 1 in<sup>2</sup> pad of 2 oz copper.

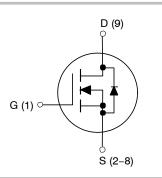


### **ON Semiconductor®**

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MO-299A CASE 100CU



#### **ORDERING INFORMATION**

| Device           | Package              | Marking  |
|------------------|----------------------|----------|
| NVBLS0D7N04M8TXG | MO-299A<br>(Pb-Free) | 0D7N04M8 |

#### NVBLS0D7N04M8

#### Table 1. ELECTRICAL CHARACTERISTICS (T<sub>J</sub> = 25°C unless otherwise noted)

| Symbol              | Parameter                         | Test Conditions   |   | Min | Тур   | Max  | Units |
|---------------------|-----------------------------------|---|---|-----|-------|------|-------|
| OFF CH              | ARACTERISTICS                     |   |   | •   | •     | •    | -     |
| B <sub>VDSS</sub>   | Drain-to-Source Breakdown Voltage | $I_D$ = 250 $\mu$ A, $V_{GS}$ = 0 V                                 |   | 40  | -     | -    | V     |
| I <sub>DSS</sub>    | Drain-to-Source Leakage Current   | V <sub>DS</sub> = 40 V,<br>V <sub>GS</sub> = 0 V                    | T <sub>J</sub> = 25°C                               | -   | -     | 1    | μA    |
|                     |                                   | V <sub>GS</sub> = 0 V   | T <sub>J</sub> = 175°C (Note 4)                     | -   | -     | 1    | mA    |
| I <sub>GSS</sub>    | Gate-to-Source Leakage Current    | V <sub>GS</sub> = ±20 V   |   | -   | -     | ±100 | nA    |
| ON CHA              | RACTERISTICS                      |   |   |     |       |      |       |
| V <sub>GS(th)</sub> | Gate-to-Source Threshold Voltage  | $V_{GS} = V_{DS},$  | $V_{GS} = V_{DS}$ , $I_D = 250 \ \mu A$             |     | 3.3   | 4.0  | V     |
| R <sub>DS(on)</sub> | Drain-to-Source On Resistance     | $I_D = 80 \text{ A}, V_{GS} = 10 \text{ V}$                         | $T_J = 25^{\circ}C$                                 | -   | 0.59  | 0.75 | mΩ    |
| DYNAMI              | C CHARACTERISTICS                 |   |   |     |       |      |       |
| C <sub>iss</sub>    | Input Capacitance                 | V <sub>DS</sub> = 25 V, V <sub>GS</sub> = 0 V, f = 1 MHz            |   | -   | 12000 | -    | pF    |
| Coss                | Output Capacitance                |   |   | -   | 3300  | -    | pF    |
| C <sub>rss</sub>    | Reverse Transfer Capacitance      |   |   |     | 440   | -    | pF    |
| Rg                  | Gate Resistance                   | f = 1 MHz   |   | -   | 3.3   | -    | Ω     |
| Q <sub>g(ToT)</sub> | Total Gate Charge at 10 V         | V <sub>GS</sub> = 0 to 10 V   | V <sub>DD</sub> = 32 V<br>I <sub>D</sub> = 80 A     | -   | 144   | 188  | nC    |
| Q <sub>g(th)</sub>  | Threshold Gate Charge             | $V_{GS}$ = 0 to 2 V   |   | -   | 22    | 26   | nC    |
| Q <sub>gs</sub>     | Gate-to-Source Gate Charge        |   |   | -   | 66    | -    | nC    |
| Q <sub>gd</sub>     | Gate-to-Drain "Miller" Charge     |   |   | _   | 16    | -    | nC    |
| SWITCH              | ING CHARACTERISTICS               |   |   |     |       |      |       |
| t <sub>on</sub>     | Turn-On Time                      | $V_{DD} = 20 \text{ V}, \text{ I}_{D} = 80 \text{ A},$              |   | -   | -     | 162  | ns    |
| t <sub>d(on)</sub>  | Turn-On Delay                     | v <sub>GS</sub> = 10 v,   | $V_{GS} = 10 \text{ V}, \text{ R}_{GEN} = 6 \Omega$ |     | 42    | -    | ns    |
| t <sub>r</sub>      | Rise Time                         | -   |   | -   | 73    | -    | ns    |
| t <sub>d(off)</sub> | Turn-Off Delay                    |   |   | -   | 83    | -    | ns    |
| t <sub>f</sub>      | Fall Time                         |   |   | -   | 50    | -    | ns    |
| t <sub>off</sub>    | Turn–Off Time                     |   |   | -   | -     | 279  | ns    |
| DRAIN-              | SOURCE DIODE CHARACTERISTICS      |   |   |     |       |      |       |
| V <sub>SD</sub>     | Source-to-Drain Diode Voltage     | $I_{SD} = 80 \text{ A}, \text{ V}_{GS} = 0 \text{ V}$               |   | -   | -     | 1.25 | V     |
|                     |                                   | I <sub>SD</sub> = 40 A,   | $V_{GS} = 0 V$                                      | -   | -     | 1.2  | V     |
| t <sub>rr</sub>     | Reverse-Recovery Time             | $I_{F} = 80 \text{ A}, dI_{SD}/d_{t} = 100 \text{ A}/\mu \text{s},$ |   | -   | 111   | 129  | ns    |
|                     | Vpp = 32 V                        |   | = 32 V  |     | T     | 1    | 1     |

Q<sub>rr</sub>

Reverse-Recovery Charge

4. The maximum value is specified by design at  $T_J = 175^{\circ}$ C. Product is not tested to this condition in production. Product parametric performance is indicated in the Electrical Characteristics for the listed test conditions, unless otherwise noted. Product performance may not be indicated by the Electrical Characteristics if operated under different conditions.

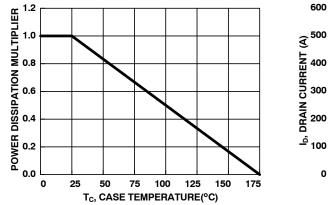
 $V_{DD} = 32 V$ 

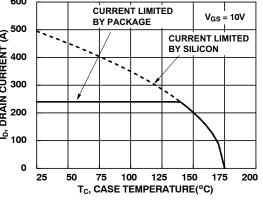
178

214

nC

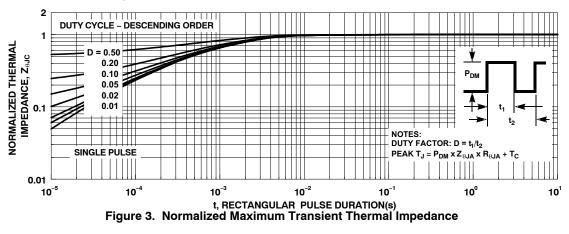
## **Typical Characteristics**











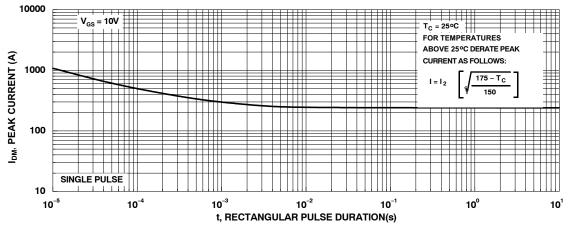


Figure 4. Peak Current Capability

# **Typical Characteristics**

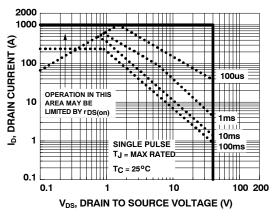
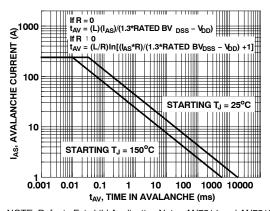
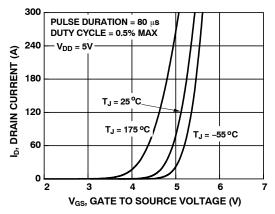


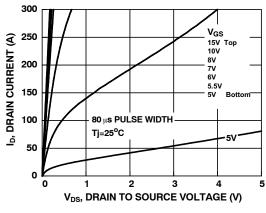
Figure 5. Forward Bias Safe Operating Area













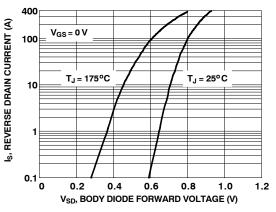
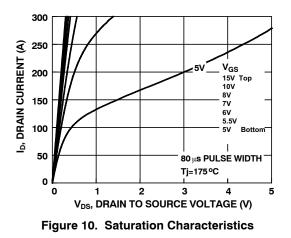
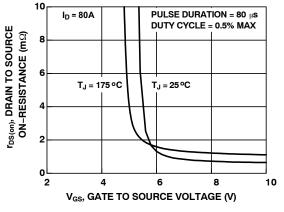


Figure 8. Forward Diode Characteristics



## **Typical Characteristics**





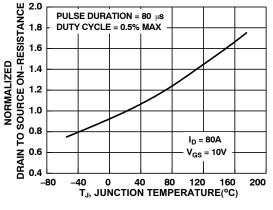
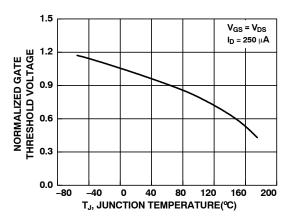
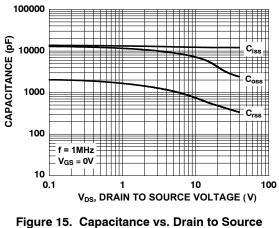


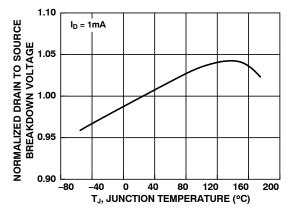
Figure 12. Normalized R<sub>DSON</sub> vs. Junction Temperature

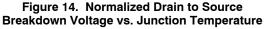


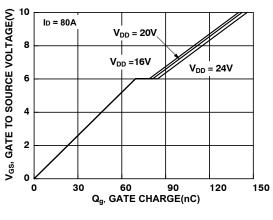




Voltage



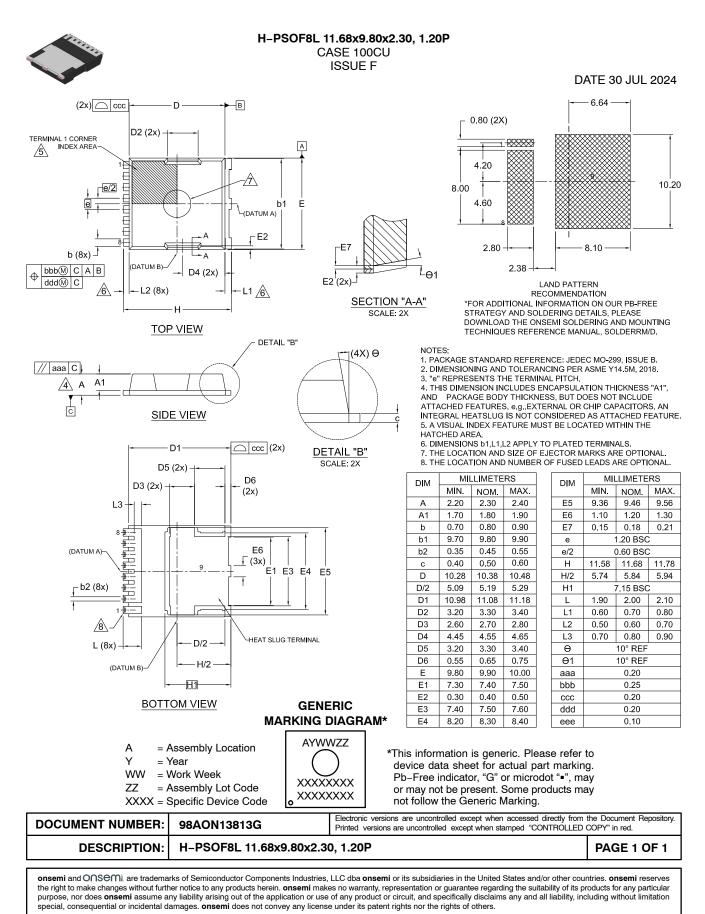






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